



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL380 G5 (2.33GHz, Intel Xeon processor 5140)

SPECint2000 = 2441
SPECint_base2000 = 2432

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	96.7	1448	96.6	1449	
175.vpr	1400	79.8	1755	76.8	1823	
176.gcc	1100	40.0	2747	40.0	2747	
181.mcf	1800	48.6	3702	48.6	3703	
186.crafty	1000	46.7	2142	46.7	2140	
197.parser	1800	92.9	1937	92.6	1944	
252.eon	1300	42.6	3052	42.7	3043	
253.perlbmk	1800	63.9	2817	63.5	2834	
254.gap	1100	46.4	2369	46.4	2373	
255.vortex	1900	46.8	4061	46.7	4065	
256.bzip2	1500	83.1	1805	83.1	1805	
300.twolf	3000	112	2669	112	2669	

Hardware

CPU: Intel Xeon processor 5140 (2.33GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz: 2330
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2048 MB PC2-5300F)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

256.bzip2: basepeak=1
300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode