### Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor X5355, 2.66 GHz

**SPECfp_rate2000 = 104**

**SPECfp_rate_base2000 = 104**

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<th>Base Ratio</th>
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</table>

**Notes/Tuning Information**

**GENERAL**

+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
for Fortran and C programs: -fast +FDO

Peak tuning flags
same as baseline (basepeak=true set globally)

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**Hardware**

- **CPU:** Intel Xeon processor X5355 (2.66 GHz, 2x4MB L2, 1333 MHz system bus)
- **CPU MHz:** 2666
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip
- **CPU(s) orderable:** 1,2 chips
- **Parallel:** No
- **Primary Cache:** 32KB(I) + 32KB(D) on chip, per core
- **Secondary Cache:** 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
- **L3 Cache:** N/A
- **Other Cache:** N/A
- **Memory:** 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
- **Disk Subsystem:** Seagate ST336754SS (SAS, 15.4krpm, 36GB)
- **Other Hardware:** none

**Software**

- **Operating System:** 64-Bit SUSE LINUX Enterprise Server 10
- **Compiler:** Intel C++ and Fortran Compiler 9.1 for EM64T
  Build 20060816 (for 64-bit applications)
- **File System:** ext2
- **System State:** Multi-user runlevel 3
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Notes/Tuning Information (Continued)

The system bus runs at 1333 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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