



SPEC® CINT2006 Result

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Sun Microsystems Ultra Enterprise 2

SPECint®2006 = --

SPECint_base2006 = 1.00

CPU2006 license: 6

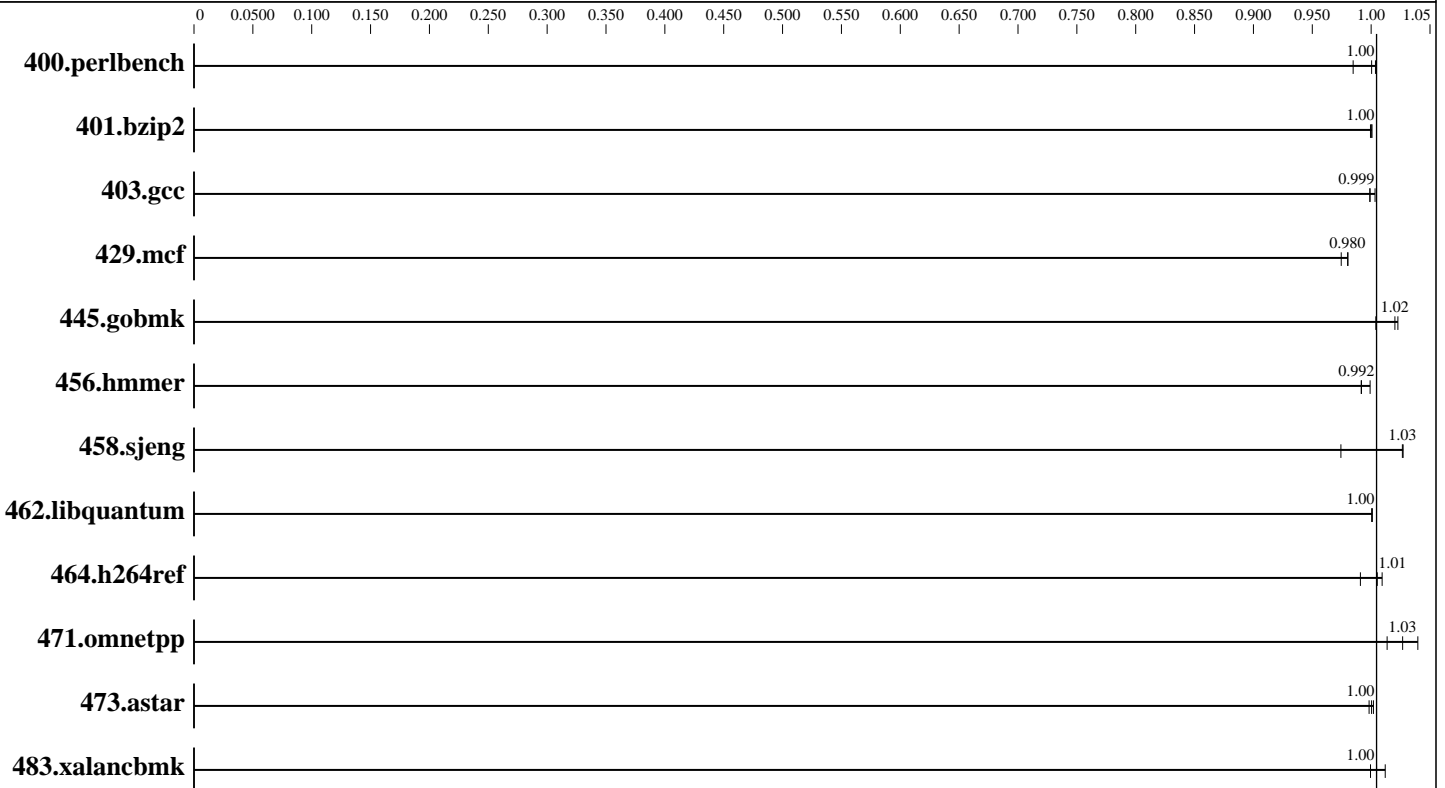
Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Mar-2006

Hardware Availability: Apr-1997

Software Availability: Oct-2005



SPECint_base2006 = 1.00

Hardware

CPU Name: UltraSPARC II
 CPU Characteristics:
 CPU MHz: 296
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 16 KB I + 16 KB D on chip per chip
 Secondary Cache: 2 MB I+D off chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB
 Disk Subsystem: 2x36GB 10K RPM SCSI (1 sys disk, 1 for SPEC)
 Other Hardware: None

Software

Operating System: Solaris 10 3/05
 Compiler: Sun Studio 11
 Auto Parallel: No
 File System: ufs
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	9922	0.985	9734	1.00	<u>9766</u>	<u>1.00</u>						
401.bzip2	9656	0.999	<u>9644</u>	<u>1.00</u>	9644	1.00						
403.gcc	8059	0.999	<u>8058</u>	<u>0.999</u>	8023	1.00						
429.mcf	9358	0.975	<u>9306</u>	<u>0.980</u>	9303	0.980						
445.gobmk	10257	1.02	<u>10283</u>	<u>1.02</u>	10449	1.00						
456.hammer	<u>9408</u>	<u>0.992</u>	9409	0.992	9338	0.999						
458.sjeng	11780	1.03	<u>11787</u>	<u>1.03</u>	12419	0.974						
462.libquantum	20708	1.00	20702	1.00	<u>20704</u>	<u>1.00</u>						
464.h264ref	<u>22015</u>	<u>1.01</u>	21925	1.01	22334	0.991						
471.omnetpp	6012	1.04	<u>6087</u>	<u>1.03</u>	6166	1.01						
473.astar	7032	0.998	7006	1.00	<u>7017</u>	<u>1.00</u>						
483.xalancbmk	6818	1.01	<u>6869</u>	<u>1.00</u>	6904	0.999						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack limit set to 131072

Platform Notes

Although two CPUs were enabled during testing,
the SPEC CPU2006 benchmarks used only 1 CPU.

Base Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

Base Portability Flags

400.perlbench: -DSPEC_CPU_SOLARIS_SPARC
403.gcc: -DSPEC_CPU_SOLARIS
462.libquantum: -DSPEC_CPU_SOLARIS
473.astar: -DSPEC_CPU_BIG_ENDIAN
483.xalancbmk: -DSPEC_CPU_SOLARIS



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Base Optimization Flags

C benchmarks:
-g -fast -xpagesize=4M
C++ benchmarks:
-g0 -library=stlport4 -fast -xpagesize=4M

Base Other Flags

C benchmarks:
-V
C++ benchmarks:
-verbose=version

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/sun-studio.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/sun-studio.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v89.
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