## SPEC® CINT2006 Result

**Apple Computer, Inc.**

iMac 20-inch 2.0GHz Intel Core Duo

<table>
<thead>
<tr>
<th>SPECint®2006</th>
<th>10.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_base2006</td>
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</table>

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<tr>
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### Hardware

- **CPU Name:** Intel Core Duo T2500
- **CPU Characteristics:**
  - CPU MHz: 2000
  - FPU: Integrated
  - CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
  - CPU(s) orderable: 1 chip
  - Primary Cache: 32 KB I + 32 KB D on chip per core
  - Secondary Cache: 2 MB I+D on chip per chip
  - L3 Cache: None
  - Other Cache: None
- **Memory:** 1 GB, DDR2 667 MHz, CL5
- **Disk Subsystem:** SATA Maxtor 6L250M0, 250 GB
- **Other Hardware:** None

### Software

- **Operating System:** Mac OS X, v10.4.6
- **Compiler:**
  - Intel C++ Compiler for Mac OS X, v9.1
  - Intel Fortran Compiler for Mac OS X, v9.1 (Build 20060323)
- **Auto Parallel:** No
- **File System:** HFS+
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** None

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### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECint2006</th>
<th>SPECint_base2006</th>
</tr>
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<tbody>
<tr>
<td>400.perlibench</td>
<td>401.bzip2</td>
<td>8.40</td>
</tr>
<tr>
<td>403.gcc</td>
<td>429.mcf</td>
<td>12.6</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>456.hmmer</td>
<td>8.03</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>462.libquantum</td>
<td>11.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>471.omnetpp</td>
<td>6.76</td>
</tr>
<tr>
<td>473.astar</td>
<td>483.xalancbmk</td>
<td>10.8</td>
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**SPECint_base2006 = 10.1**

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Results Table

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<tbody>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Memory Details:
DIMM0/Bank0: 512MB, DDR2 SDRAM, 667 MHz, CL5
DIMM1/Bank1: 512MB, DDR2 SDRAM, 667 MHz, CL5

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

403.gcc: -DSPEC_CPU_MACOSX
462.libquantum: -DSPEC_CPU_MACOSX
483.xalancbmk: -DSPEC_CPU_MACOSX

Base Optimization Flags

C benchmarks:
-fast

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### Base Optimization Flags (Continued)

- C++ benchmarks:
  - `-fast`

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### Peak Compiler Invocation

- C benchmarks:
  - `icc`
- C++ benchmarks:
  - `icpc`

---

### Peak Portability Flags

- `403.gcc`: `-DSPEC_CPU_MACOSX`
- `462.libquantum`: `-DSPEC_CPU_MACOSX`
- `483.xalancbmk`: `-DSPEC_CPU_MACOSX`

---

### Peak Optimization Flags

- C benchmarks:
  - `400.perlbench`: `basepeak = yes`
  - `401.bzip2`: `basepeak = yes`
  - `403.gcc`: `basepeak = yes`
  - `429.mcf`: `basepeak = yes`
  - `445.gobmk`: `basepeak = yes`
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  - `458.sjeng`: `basepeak = yes`
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  - `464.h264ref`: `basepeak = yes`
- C++ benchmarks:
  - `471.omnetpp`: `basepeak = yes`

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/macosx-iccifort-v91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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