



SPEC® CINT2006 Result

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Dell Inc.

Dell Precision 380 (3.73 GHz, Pentium Extreme Edition 965)

SPECint®2006 = --

SPECint_base2006 = 11.6

CPU2006 license: 55

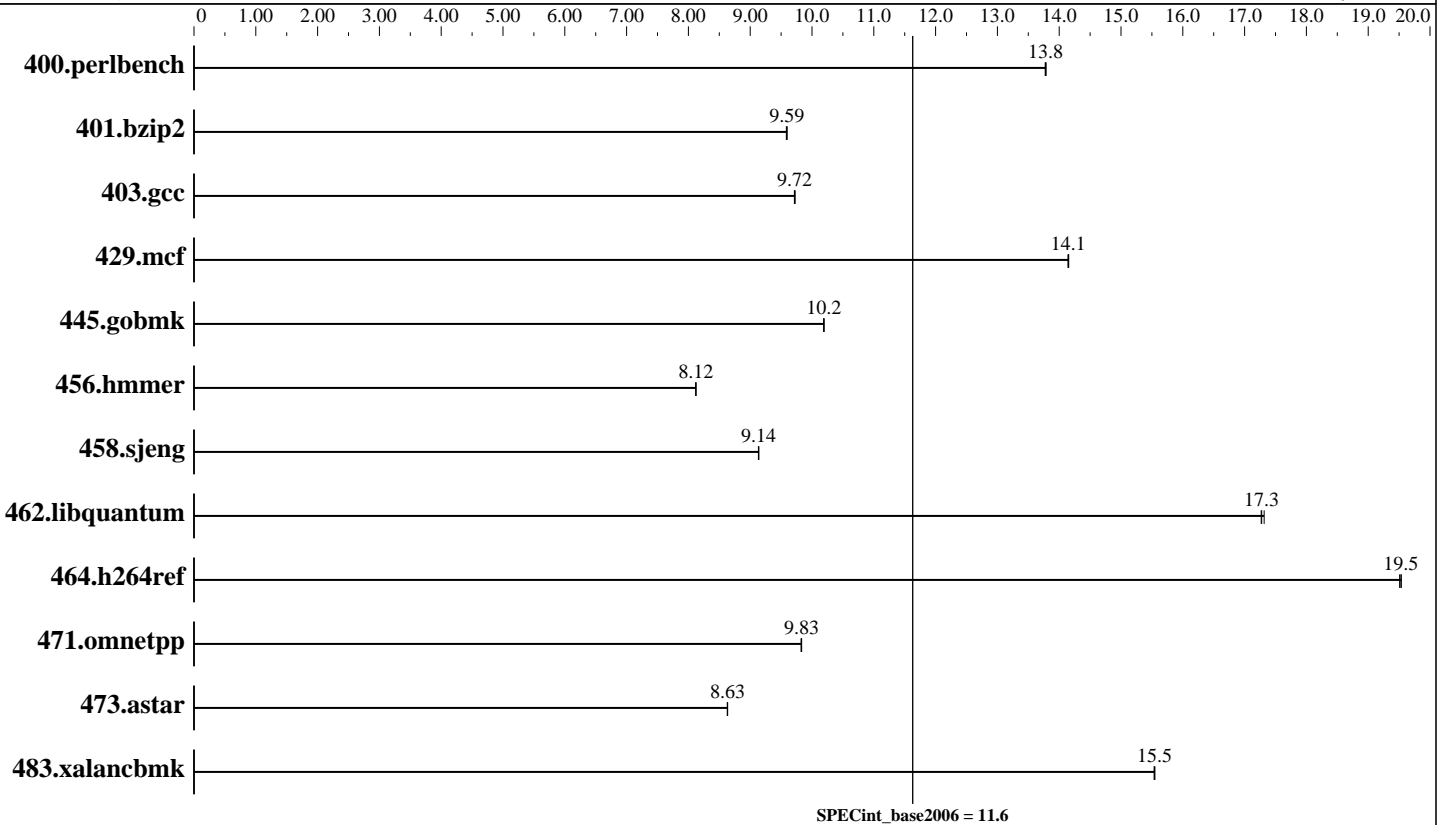
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2006

Hardware Availability: May-2006

Software Availability: May-2006



Hardware

CPU Name: Intel Pentium Extreme Edition 965
 CPU Characteristics: 3733
 CPU MHz: Integrated
 FPU: 2 cores, 1 chip, 2 cores/chip
 CPU(s) enabled: 1 chip
 CPU(s) orderable: 12 K micro-ops I + 16 KB D on chip per core
 Primary Cache: 2 MB I+D on chip per core
 Secondary Cache: None
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB 667MHz ECC CL5 DDR2 SDRAM)
 Disk Subsystem: 1 x 80GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional x64 Edition
 Compiler: Intel C++ Compiler 9.1 for IA32 (20060323Z)
 Microsoft Visual Studio .NET 2003
 MicroQuill SmartHeap Library 7.4
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	709	13.8	709	13.8	709	13.8						
401.bzip2	1007	9.59	1006	9.60	1006	9.59						
403.gcc	828	9.73	828	9.72	829	9.72						
429.mcf	644	14.2	645	14.1	645	14.1						
445.gobmk	1029	10.2	1030	10.2	1029	10.2						
456.hammer	1149	8.12	1149	8.12	1149	8.12						
458.sjeng	1324	9.14	1325	9.13	1324	9.14						
462.libquantum	1199	17.3	1200	17.3	1197	17.3						
464.h264ref	1133	19.5	1134	19.5	1133	19.5						
471.omnetpp	636	9.83	636	9.82	636	9.83						
473.astar	813	8.63	814	8.63	813	8.63						
483.xalancbmk	444	15.5	444	15.5	444	15.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v90.
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