



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = --

ProLiant DL380 G4 (3.8GHz, Intel Xeon Processor)

SPECint_base2006 = 11.4

CPU2006 license: 3

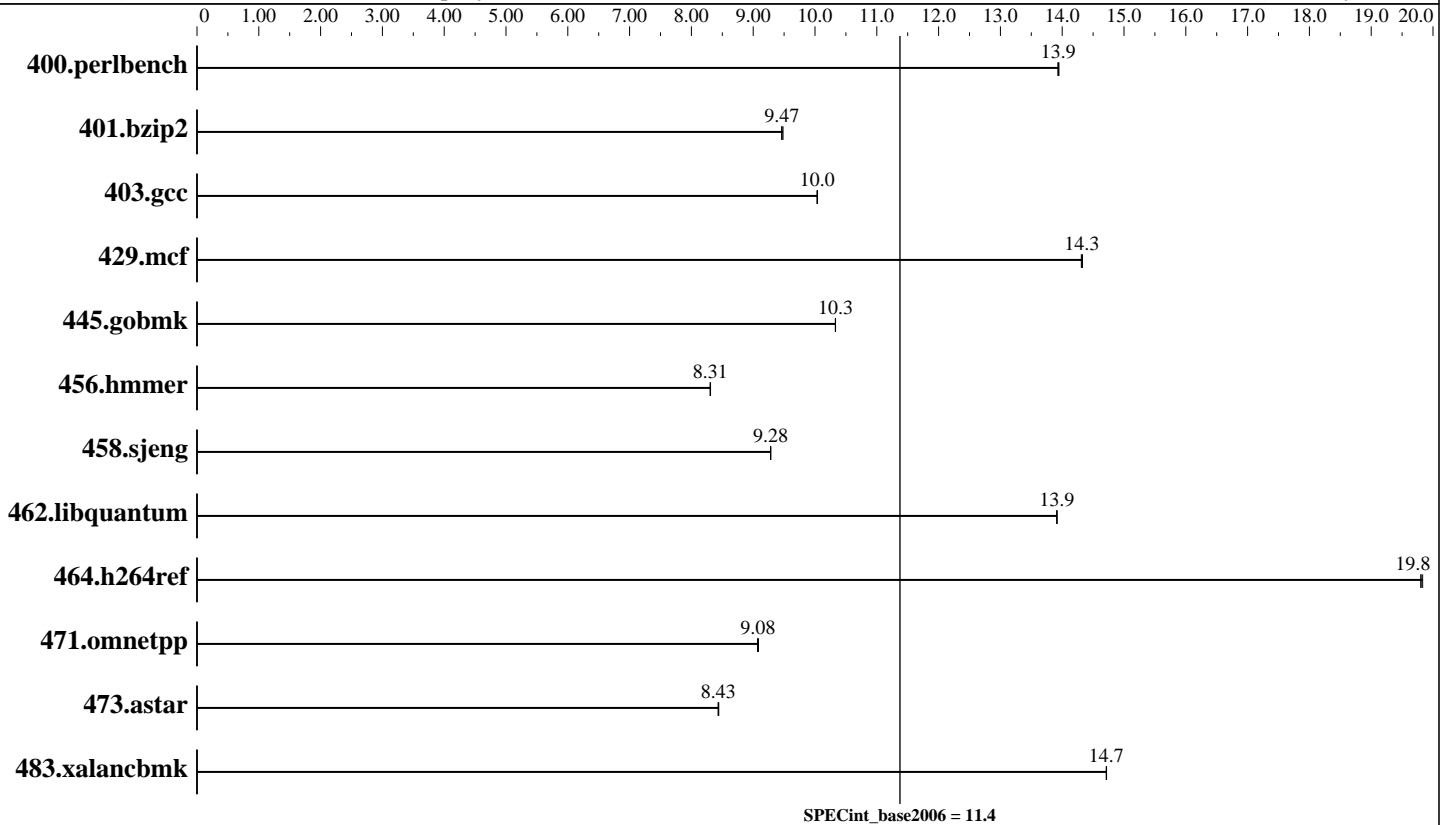
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2006

Hardware Availability: Sep-2005

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon
 CPU Characteristics: 3.8GHz, 2MB L2, 800MHz bus
 CPU MHz: 3800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per chip
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-3200)
 Disk Subsystem: 146GB 10k U320 SCSI
 Other Hardware: None

Software

Operating System: Windows Server 2003 Ent, SP1
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (for libraries)
 MicroQuill SmartHeap Library 7.4
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	701	13.9	<u>701</u>	<u>13.9</u>	701	13.9						
401.bzip2	1018	9.48	1020	9.46	<u>1019</u>	<u>9.47</u>						
403.gcc	802	10.0	<u>802</u>	<u>10.0</u>	802	10.0						
429.mcf	637	14.3	<u>637</u>	<u>14.3</u>	637	14.3						
445.gobmk	1015	10.3	<u>1015</u>	<u>10.3</u>	1016	10.3						
456.hammer	1123	8.31	<u>1123</u>	<u>8.31</u>	1123	8.30						
458.sjeng	1303	9.29	1304	9.28	<u>1304</u>	<u>9.28</u>						
462.libquantum	1489	13.9	<u>1489</u>	<u>13.9</u>	1489	13.9						
464.h264ref	1116	19.8	1118	19.8	<u>1117</u>	<u>19.8</u>						
471.omnetpp	688	9.09	<u>689</u>	<u>9.08</u>	689	9.07						
473.astar	831	8.44	832	8.43	<u>832</u>	<u>8.43</u>						
483.xalancbmk	469	14.7	469	14.7	<u>469</u>	<u>14.7</u>						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v90.
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