SPEC® CINT2006 Result

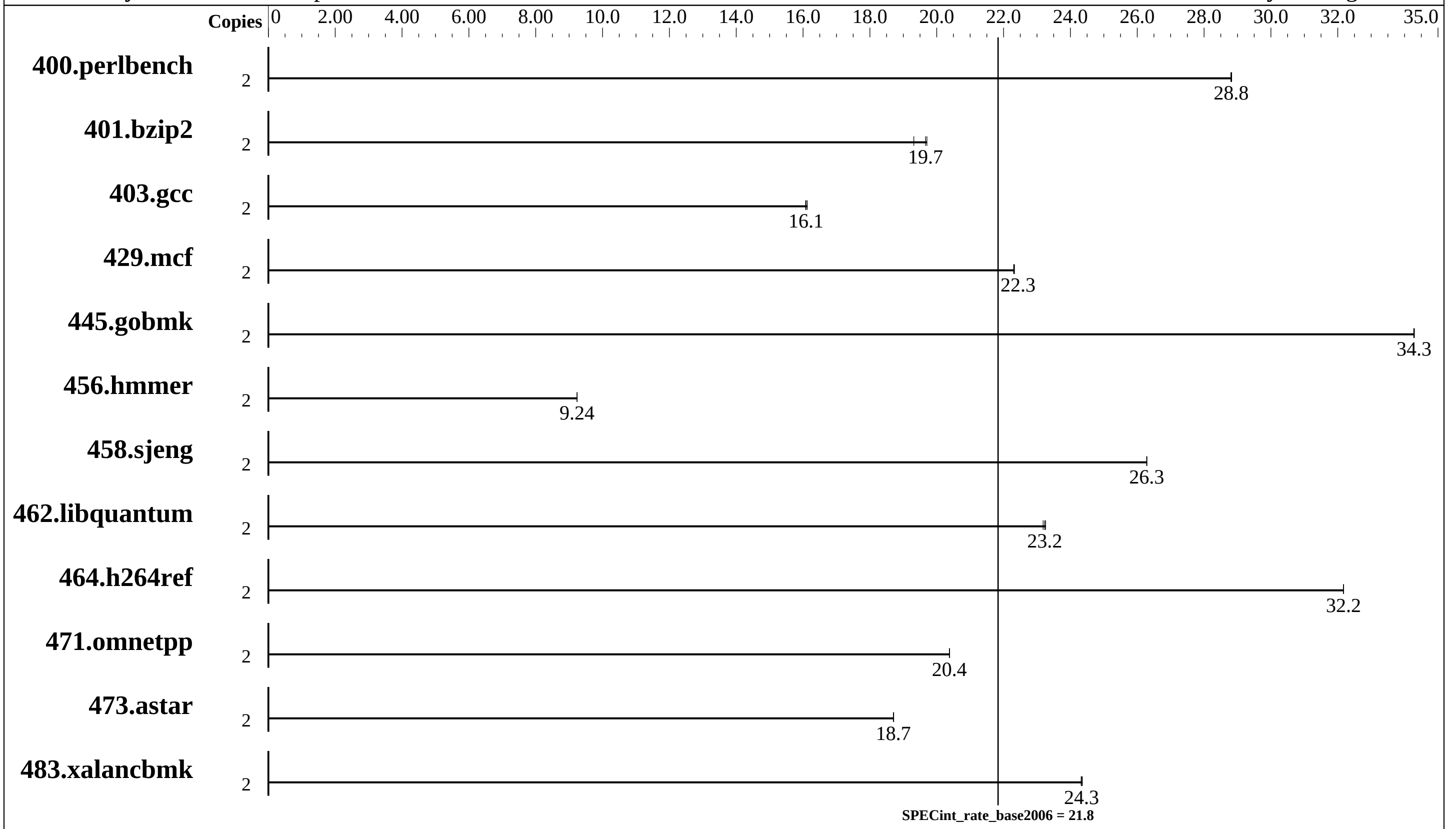
ASUS Computer International
(Test Sponsor: Intel Corporation)

Asus M2N32-SLI Deluxe
(AMD Athlon 64 FX-62)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 21.8

CPU2006 license: 13		**Test date:**	Aug-2006
Test sponsor:	Intel Corporation	**Hardware Availability:**	Jul-2006
Tested by:	Intel Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	AMD Athlon 64 FX-62
CPU Characteristics:	2.80 GHz, 2x1MB L2 per core
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 1GB Corsair TWIN2X2048-8500C5 DDR2 1066 CL5-5-5)
Disk Subsystem:	Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)
Other Hardware:	None

Software

Operating System:	Windows XP Professional SP2 32-bit
Compiler:	Intel C++ Compiler 9.1 for IA32 Build 20060816Z Microsoft Visual Studio .Net 2003 (for libraries) SmartHeap Library Version 8.0 from http://www.microquill.com/
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**678**	**28.8**	678	28.8	678	28.8							
401.bzip2	2	999	19.3	**981**	**19.7**	979	19.7							
403.gcc	2	998	16.1	**1001**	**16.1**	1001	16.1							
429.mcf	2	818	22.3	817	22.3	**817**	**22.3**							
445.gobmk	2	612	34.3	612	34.3	**612**	**34.3**							
456.hmmer	2	2019	9.24	**2020**	**9.24**	2020	9.24							
458.sjeng	2	921	26.3	**921**	**26.3**	921	26.3							
462.libquantum	2	1782	23.3	**1784**	**23.2**	1787	23.2							
464.h264ref	2	1376	32.2	1376	32.2	**1376**	**32.2**							
471.omnetpp	2	**613**	**20.4**	613	20.4	613	20.4							
473.astar	2	751	18.7	750	18.7	**751**	**18.7**							
483.xalancbmk	2	**567**	**24.3**	567	24.4	567	24.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Memory timings set manually to DDR2-800 5-5-5-15 in the bios
The SUT was built with a discrete PCIe graphics card (ATI Radeon X1900 XTX)
and an ATX 550W power supply. The 2 DIMMs were configured in dual-channel mode.
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:

```
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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