



SPEC[®] CINT2006 Result

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Dell Inc.
PowerEdge 1950

SPECint[®]2006 = 16.3
SPECint_base2006 = 15.8

CPU2006 license: 94

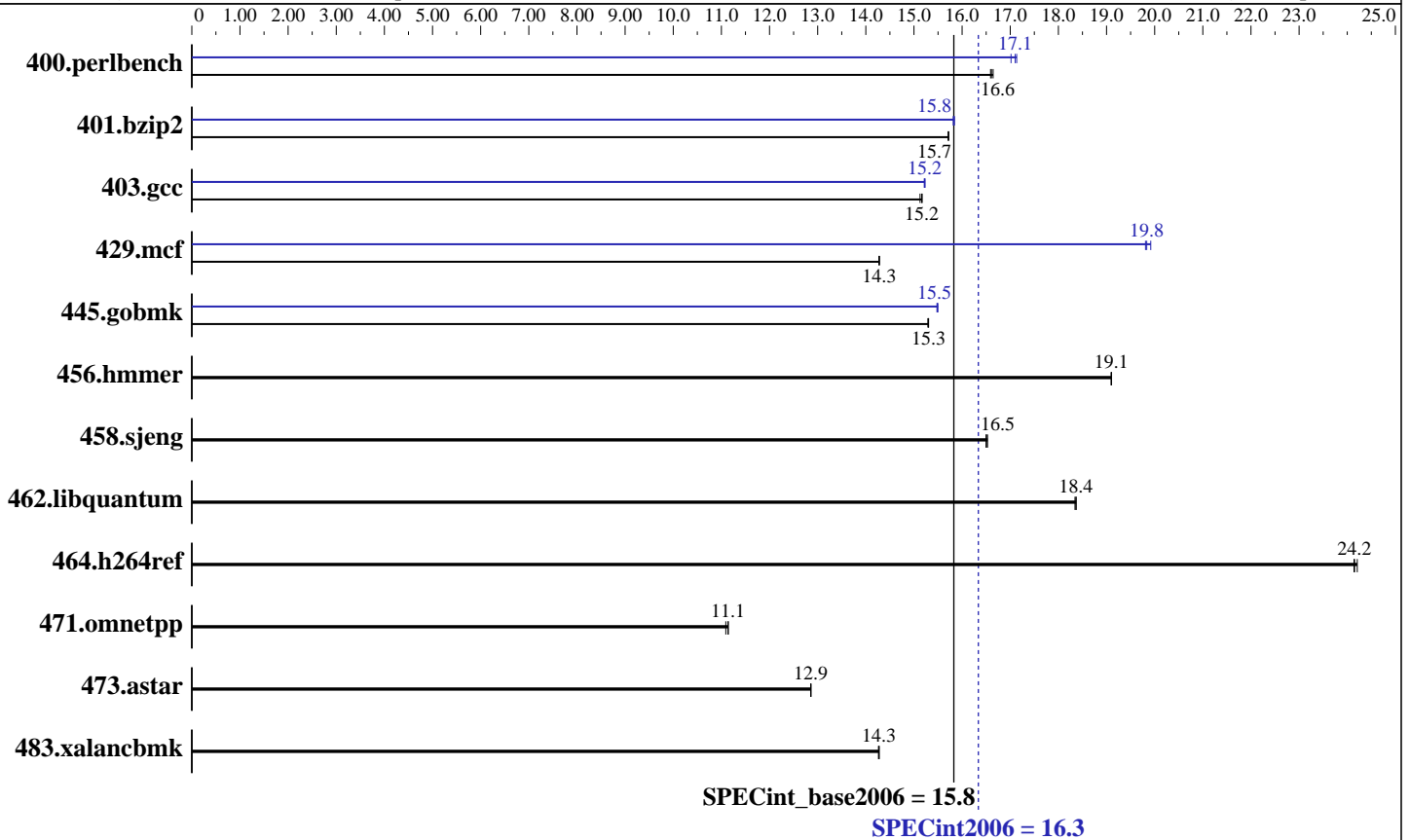
Test sponsor: The Portland Group

Tested by: The Portland Group

Test date: Sep-2006

Hardware Availability: Jul-2006

Software Availability: Sep-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 to 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x 1GB, Samsung M395T2953CZ4 DDR2 FBD 667 CL5-5-5)
 Disk Subsystem: Hitachi Deskstar SATA, 164 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SLES 10 (Kernel 2.6.16.21-0.8-smp)
 Compiler: The Portland Group (PGI)
 PGI pgf90 6.2-3 Fortran Compiler
 PGI pgcc 6.2-3 C Compiler
 PGI pgCC 6.2-3 C++ Compiler
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	587	16.6	588	16.6	589	16.6	574	17.0	570	17.1	571	17.1
401.bzip2	614	15.7	614	15.7	614	15.7	609	15.8	610	15.8	610	15.8
403.gcc	531	15.2	531	15.2	532	15.1	529	15.2	529	15.2	529	15.2
429.mcf	639	14.3	639	14.3	639	14.3	458	19.9	460	19.8	460	19.8
445.gobmk	686	15.3	685	15.3	686	15.3	678	15.5	678	15.5	677	15.5
456.hmmer	489	19.1	488	19.1	488	19.1	489	19.1	488	19.1	488	19.1
458.sjeng	732	16.5	733	16.5	733	16.5	732	16.5	733	16.5	733	16.5
462.libquantum	1130	18.3	1128	18.4	1128	18.4	1130	18.3	1128	18.4	1128	18.4
464.h264ref	914	24.2	917	24.1	916	24.2	914	24.2	917	24.1	916	24.2
471.omnetpp	561	11.1	561	11.1	563	11.1	561	11.1	561	11.1	563	11.1
473.astar	546	12.9	546	12.9	546	12.9	546	12.9	546	12.9	546	12.9
483.xalancbmk	484	14.3	483	14.3	483	14.3	484	14.3	483	14.3	483	14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Environment stack size set to 'unlimited'
The 4 1GB memory modules populated the first DIMM socket of each channel (0-3).

Compiler Invocation

C benchmarks:
pgcc

C++ benchmarks:
pgCC

Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64 -DSPEC_CPU_HAVE_BOOL
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64

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Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
 -fastsse -Mipa=fast -Mipa=inline -Msmartalloc -tp core2-64

C++ benchmarks:
 -fastsse -Mipa=fast -Mipa=inline -Msmartalloc -tp core2

Peak Optimization Flags

C benchmarks:

400.perlbench: -Mpfi(pass 1) -Mipa=fast(pass 2) -Mipa=inline(pass 2)
 -Mpfo(pass 2) -fastsse -Msmartalloc -tp core2-64

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: -Mpfi(pass 1) -Mipa=fast(pass 2) -Mipa=inline(pass 2)
 -Mpfo(pass 2) -fastsse -tp core2

445.gobmk: -Mpfi(pass 1) -Mipa=fast(pass 2) -Mipa=inline(pass 2)
 -Mpfo(pass 2) -fastsse -Msmartalloc -Msafeptr
 -tp core2-64

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Other Flags

C benchmarks:
-w

C++ benchmarks:
-w

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/pgi62_flags.20090715.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/pgi62_flags.20090715.xml

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