**Hewlett-Packard Company**

HP Integrity rx3600  
(1.6GHz/18MB Dual-Core Intel Itanium 2)

**SPECint\textsuperscript{®} rate\textsubscript{2006} = 45.9**  
**SPECint\textsuperscript{®} rate\textsubscript{base2006} = 43.6**

**Hardware**

CPU Name: Dual-Core Intel Itanium 2 9040  
CPU Characteristics: 1.6GHz/18MB, 533MHz FSB  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1-2 chips  
Primary Cache: 16 KB I + 16 KB D on chip per core  
Secondary Cache: 1 MB I + 256 KB D on chip per core  
L3 Cache: 9 MB I+D on chip per core  
Other Cache: None  
Memory: 16 GB (8x2GB DIMMs, AD124A 8-DIMM memory carrier)  
Disk Subsystem: 2x73GB 10K RPM SAS (mirrored)  
Other Hardware: None

**Software**

Operating System: Red Hat Enterprise Linux AS release 4 (Update 4)  
Compiler: Intel C++ Compiler for Itanium version 9.1 (Build 20060818)  
Auto Parallel: No  
File System: ext3  
System State: Multi-user  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other Software: MicroQuill Smartheap 8.0

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
Hewlett-Packard Company
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SPECint_rate_base2006 = 43.6

Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

stacksize set to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64

Continued on next page
**SPEC CINT2006 Result**

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<th>CPU2006 license: 03</th>
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<td>Software Availability: Nov-2006</td>
</tr>
</tbody>
</table>

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**Base Portability Flags (Continued)**

473.astar: -DSPEC_CPU_LP64
483.xalancbm: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

**Base Optimization Flags**

C benchmarks:
- fast -IPF_fp_relaxed -ansi-alias

C++ benchmarks:
- fast -IPF_fp_relaxed -ansi-alias -Wl,-z,muldefs  
  /opt/SmartHeap_8/lib/lsmartheapC64.a  
  /opt/SmartHeap_8/lib/lsmartheap64.a

**Peak Compiler Invocation**

C benchmarks:
icc

C++ benchmarks:
icpc

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
- 400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed -ansi-alias
  - 401.bzip2: Same as 400.perlbench
  - 403.gcc: Same as 400.perlbench
  - 429.mcf: basepeak = yes
  - 445.gobmk: Same as 400.perlbench
  - 456.hmmer: basepeak = yes

Continued on next page
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Test date: Nov-2006
Hardware Availability: Nov-2006
Software Availability: Nov-2006

Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias -inline-factor=150 -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

483.xalancbmk: Same as 471.omnetpp

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/IPF_intel91_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/IPF_intel91_flags.xml

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For other inquiries, please contact webmaster@spec.org.

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