



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

**SPECint®2006 = 16.9**

### CELSIUS R640, Intel Xeon 5160 processor

**SPECint\_base2006 = 16.2**

CPU2006 license: 22

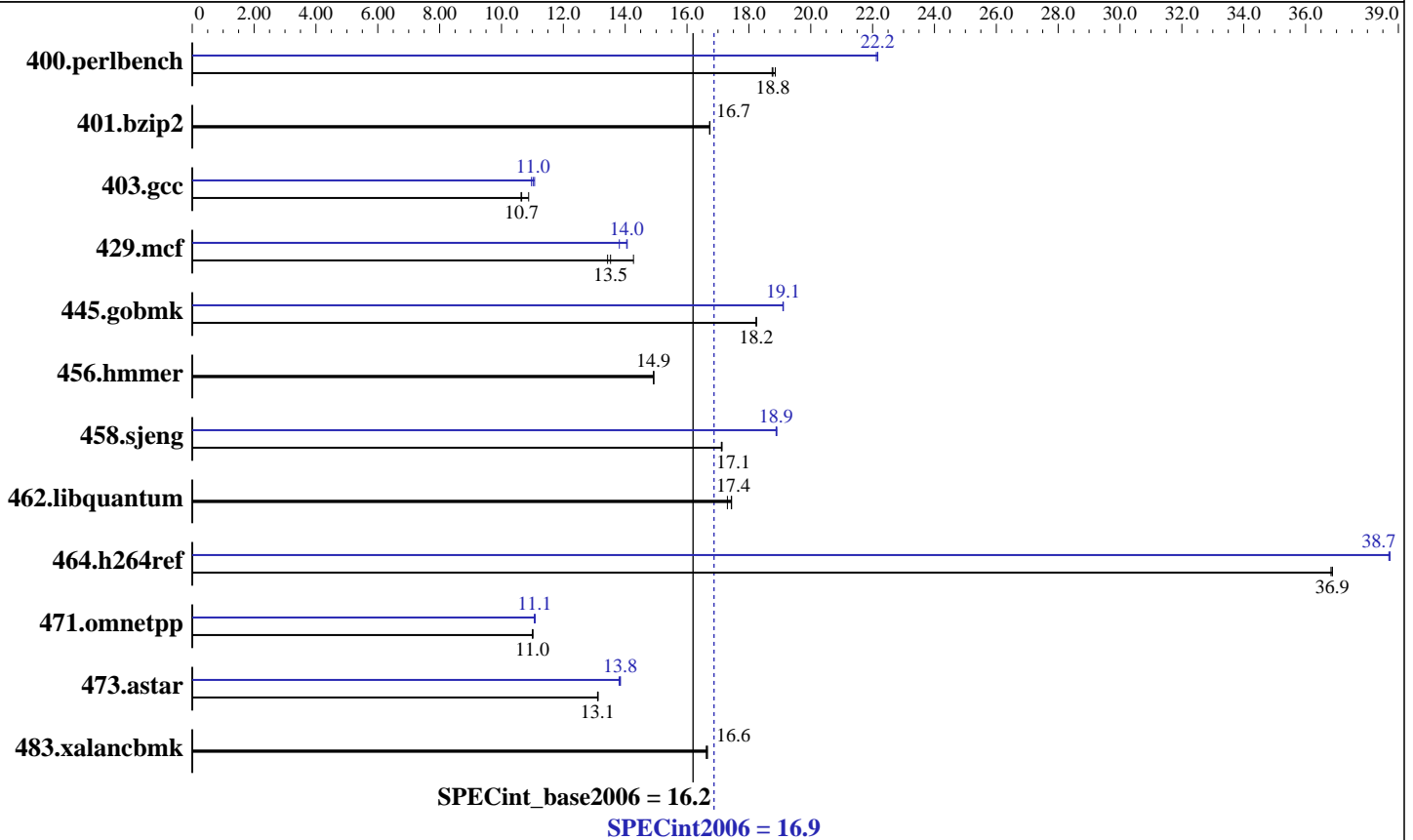
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2006

Hardware Availability: Sep-2006

Software Availability: Nov-2006



#### Hardware

CPU Name: Intel Xeon 5160  
 CPU Characteristics: Dual Core, 3.0 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: SATA II 7200 rpm  
 Other Hardware: None

#### Software

Operating System: Windows XP Professional x64 Edition  
 Compiler: Intel C++ Compiler for EM64T version 9.1  
 - Build 20061104, Package-ID W\_CC\_C\_9.1.033  
 Microsoft Visual Studio 2005 (libr. & linker)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	521	18.8	518	18.9	<u>520</u>	<u>18.8</u>	<u>441</u>	<u>22.2</u>	441	22.2	442	22.1
401.bzip2	576	16.7	577	16.7	<u>577</u>	<u>16.7</u>	576	16.7	577	16.7	<u>577</u>	<u>16.7</u>
403.gcc	740	10.9	<u>756</u>	<u>10.7</u>	757	10.6	734	11.0	<u>730</u>	<u>11.0</u>	727	11.1
429.mcf	679	13.4	639	14.3	<u>674</u>	<u>13.5</u>	660	13.8	648	14.1	<u>649</u>	<u>14.0</u>
445.gobmk	575	18.2	<u>575</u>	<u>18.2</u>	575	18.2	549	19.1	<u>549</u>	<u>19.1</u>	549	19.1
456.hmmer	<u>625</u>	<u>14.9</u>	625	14.9	625	14.9	<u>625</u>	<u>14.9</u>	625	14.9	625	14.9
458.sjeng	707	17.1	706	17.1	<u>707</u>	<u>17.1</u>	641	18.9	640	18.9	<u>641</u>	<u>18.9</u>
462.libquantum	<u>1189</u>	<u>17.4</u>	1188	17.4	1197	17.3	<u>1189</u>	<u>17.4</u>	1188	17.4	1197	17.3
464.h264ref	<u>600</u>	<u>36.9</u>	601	36.8	600	36.9	572	38.7	571	38.7	<u>571</u>	<u>38.7</u>
471.omnetpp	<u>568</u>	<u>11.0</u>	567	11.0	568	11.0	<u>565</u>	<u>11.1</u>	565	11.1	564	11.1
473.astar	536	13.1	535	13.1	<u>536</u>	<u>13.1</u>	509	13.8	<u>507</u>	<u>13.8</u>	507	13.8
483.xalancbmk	<u>415</u>	<u>16.6</u>	415	16.6	414	16.7	<u>415</u>	<u>16.6</u>	415	16.6	414	16.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Compiler Invocation

C benchmarks:  
icl -Qvc8 -Qc99

C++ benchmarks:  
icl -Qvc8

## Portability Flags

```

400.perlbench: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY
               -DSPEC_CPU_WIN64_X64
401.bzip2: -D_Complex= -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -D_Complex= -DSPEC_CPU_P64
445.gobmk: -D_Complex= -DSPEC_CPU_P64
456.hmmer: -D_Complex= -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64

```

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## Portability Flags (Continued)

464.h264ref: -D\_Complex= -DSPEC\_CPU\_P64 -DWIN32 -DSPEC\_CPU\_NO\_INTTYPES  
471.omnetpp: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WIN64  
473.astar: -DSPEC\_CPU\_P64  
483.xalancbmk: -DSPEC\_CPU\_P64 -Qoption,cpp,--no\_wchar\_t\_keyword

## Base Optimization Flags

C benchmarks:

-fast -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx-features -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F512000000  
shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: basepeak = yes

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: Same as 400.perlbench

456.hmmer: basepeak = yes

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
-F512000000 shlw32M.lib -link -FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes



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## Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.12.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.12.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
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