## Apple Computer, Inc.

**Xserve (3000 MHz Intel Dual-Core Xeon, 1333 MHz FSB)**

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Nov-2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Apple Computer, Inc.</td>
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<tr>
<td>Tested by:</td>
<td>Apple Computer, Inc.</td>
</tr>
<tr>
<td>SPECfp®2006 =</td>
<td>Not Run</td>
</tr>
<tr>
<td>SPECfp_base2006 =</td>
<td>14.4</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon 5160
- **CPU Characteristics:** 1333 MHz FSB
- **CPU MHz:** 3000
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 2 chips, 2 cores/chip
- **CPU(s) orderable:** 2 cpus
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 4 MB I+D on chip per chip

### Software

- **Operating System:** Mac OS X Server 10.4.8, Build 8N1200
- **Compiler:** Intel C++ Compiler for Mac OS X v9.1
- **Auto Parallel:** No
- **File System:** Journaled HFS+
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** Not Applicable

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## Apple Computer, Inc.

Xserve (3000 MHz Intel Dual-Core Xeon, 1333 MHz FSB)

### SPEC CFP2006 Result

**SPECfp2006 = Not Run**  
**SPECfp_base2006 = 14.4**

- **CPU2006 license:** 77  
- **Test sponsor:** Apple Computer, Inc.  
- **Tested by:** Apple Computer, Inc.  
- **Test date:** Nov-2006  
- **Hardware Availability:** Nov-2006  
- **Software Availability:** Nov-2006

### General Notes

- **Boot ROM Version:** XS11.0076.B00  
- **SMC Version:** 1.11f4

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>18.6</td>
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<td>20.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Memory Details:

- **BRANCH 0 CHANNEL 0/DIMM 1:** 1 GB DDR2 FB-DIMM 667 MHz
- **BRANCH 0 CHANNEL 1/DIMM 2:** 1 GB DDR2 FB-DIMM 667 MHz
- **BRANCH 1 CHANNEL 0/DIMM 3:** 1 GB DDR2 FB-DIMM 667 MHz
- **BRANCH 1 CHANNEL 1/DIMM 4:** 1 GB DDR2 FB-DIMM 667 MHz
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CPU2006 license: 77
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Test date: Nov-2006
Hardware Availability: Nov-2006
Software Availability: Nov-2006

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

435.gromacs: -nofor_main
436.cactusADM: -nofor_main
454.calculix: -nofor_main
481.wrf: -DSPEC_CPU_MACOSX -DSPEC_CPU_MACOSX_INTEL
-DSPEC_CPU_CASE_FLAG

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Base Other Flags

C benchmarks:
-Wl,-stack_addr,0xF0000000 -Wl,-stack_size,0x3F000000

C++ benchmarks:
-Wl,-stack_addr,0xF0000000 -Wl,-stack_size,0x3F000000

Fortran benchmarks:
-Wl,-stack_addr,0xF0000000 -Wl,-stack_size,0x3F000000

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SPEC CFP2006 Result

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Base Other Flags (Continued)

Benchmarks using both Fortran and C:
- Wl,-stack_addr,0xF0000000 -Wl,-stack_size,0x3F000000

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/macosx-iccifort-v91-flags.20090715.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/macosx-iccifort-v91-flags.20090715.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Originally published on 12 December 2006.