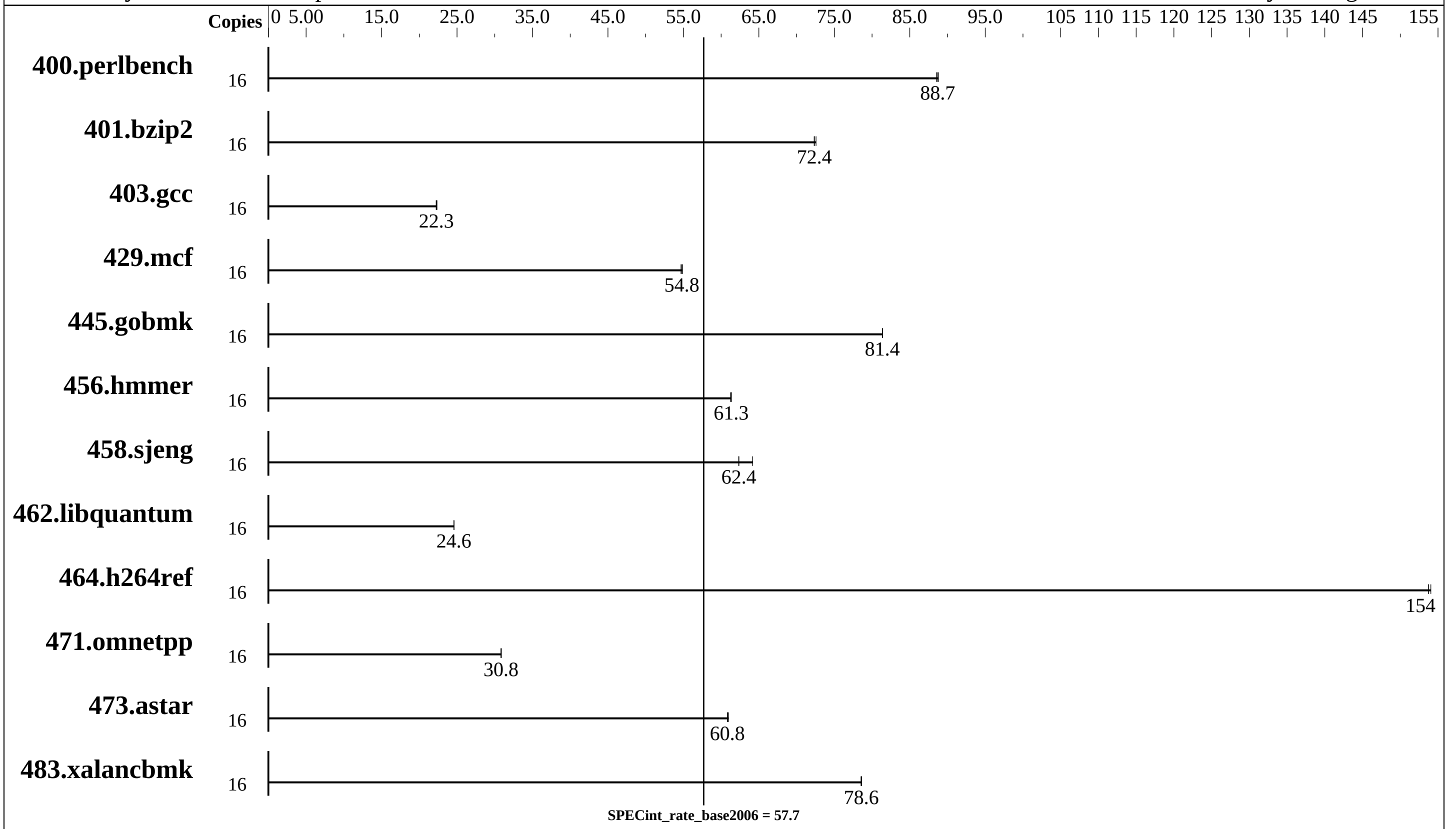
SPEC® CINT2006 Result

IBM Corporation
IBM System x3800

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 57.7

CPU2006 license: 11		**Test date:**	Dec-2006
Test sponsor:	IBM Corporation	**Hardware Availability:**	Oct-2006
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon 7130N
CPU Characteristics:	667 MHz bus
CPU MHz:	3133
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1765	88.6	1760	88.8	**1762**	**88.7**							
401.bzip2	16	**2134**	**72.4**	2127	72.6	2135	72.3							
403.gcc	16	**5782**	**22.3**	5765	22.3	5793	22.2							
429.mcf	16	**2663**	**54.8**	2659	54.9	2669	54.7							
445.gobmk	16	2062	81.4	2063	81.4	**2062**	**81.4**							
456.hmmer	16	**2434**	**61.3**	2438	61.2	2433	61.4							
458.sjeng	16	**3105**	**62.4**	3106	62.3	3017	64.2							
462.libquantum	16	13485	24.6	13479	24.6	**13480**	**24.6**							
464.h264ref	16	**2303**	**154**	2303	154	2298	154							
471.omnetpp	16	3239	30.9	3245	30.8	**3243**	**30.8**							
473.astar	16	**1846**	**60.8**	1842	61.0	1847	60.8							
483.xalancbmk	16	1404	78.6	1406	78.5	**1405**	**78.6**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.05.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.05.xml

Tested with SPEC CPU2006 v1.0.
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