SPEC® CINT2006 Result

Itautec

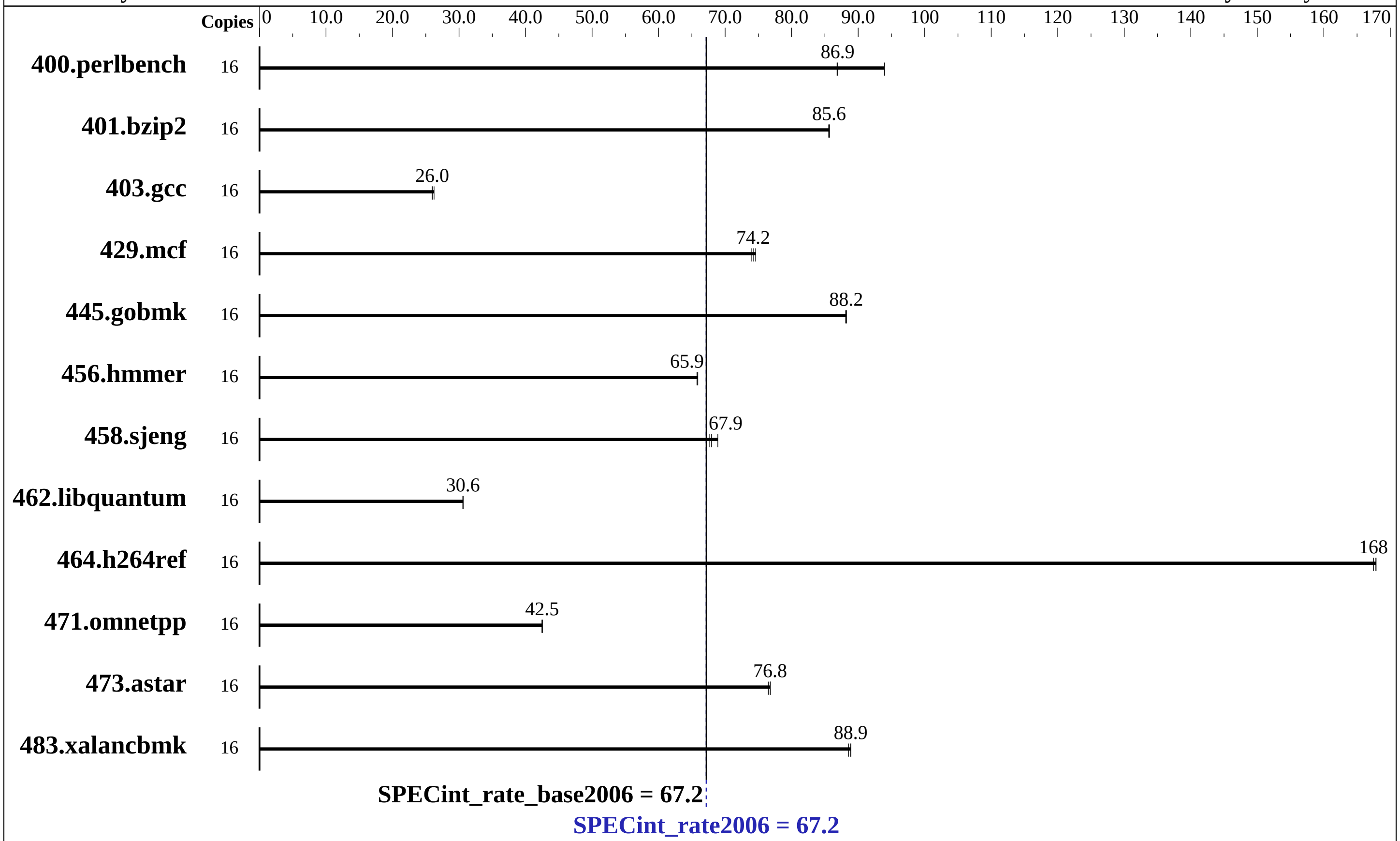
Servidor Itautec ZX440, Intel Xeon 7140M processor

SPECint®_rate2006 = 67.2

SPECint_rate_base2006 = 67.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jan-2007
Hardware Availability: Jul-2006
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon 7140M
CPU Characteristics:	Dual Core, 3.40 GHz
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1-4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (16x1GB DDR2-RAM, PC2-3200R, CAS 3-3-3)
Disk Subsystem:	36 GB SCSI, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap Library v.8.0 for SMP

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1664	93.9	**1798**	**86.9**	1800	86.8	16	1664	93.9	**1798**	**86.9**	1800	86.8
401.bzip2	16	**1803**	**85.6**	1805	85.6	1802	85.7	16	**1803**	**85.6**	1805	85.6	1802	85.7
403.gcc	16	4963	26.0	4908	26.2	**4958**	**26.0**	16	4963	26.0	4908	26.2	**4958**	**26.0**
429.mcf	16	1956	74.6	1972	74.0	**1966**	**74.2**	16	1956	74.6	1972	74.0	**1966**	**74.2**
445.gobmk	16	1902	88.2	**1903**	**88.2**	1905	88.1	16	1902	88.2	**1903**	**88.2**	1905	88.1
456.hmmer	16	**2267**	**65.9**	2265	65.9	2270	65.8	16	**2267**	**65.9**	2265	65.9	2270	65.8
458.sjeng	16	**2850**	**67.9**	2860	67.7	2810	68.9	16	**2850**	**67.9**	2860	67.7	2810	68.9
462.libquantum	16	10848	30.6	**10835**	**30.6**	10825	30.6	16	10848	30.6	**10835**	**30.6**	10825	30.6
464.h264ref	16	**2110**	**168**	2109	168	2114	167	16	**2110**	**168**	2109	168	2114	167
471.omnetpp	16	2351	42.5	2355	42.5	**2353**	**42.5**	16	2351	42.5	2355	42.5	**2353**	**42.5**
473.astar	16	1468	76.5	**1463**	**76.8**	1463	76.8	16	1468	76.5	**1463**	**76.8**	1463	76.8
483.xalancbmk	16	1242	88.9	**1242**	**88.9**	1247	88.6	16	1242	88.9	**1242**	**88.9**	1247	88.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Bios Settings default.
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlSMPMt.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlSMPMt.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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