Hewlett-Packard Company
HP Integrity rx2660
(1.6GHz/18MB Dual-Core Intel Itanium 2)

SPECint_rate2006 = 46.0
SPECint_rate_base2006 = 43.6

CPU2006 license: 03
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Nov-2006

CPU Name: Dual-Core Intel Itanium 2 9040
CPU Characteristics: 1.6GHz/18MB, 533MHz FSB
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 16 KB I + 16 KB D on chip per core
Secondary Cache: 1 MB I + 256 KB D on chip per core
L3 Cache: 9 MB I+D on chip per core
Other Cache: None
Memory: 8 GB (4x2GB DIMMs)
Disk Subsystem: 73GB 10K RPM SAS
Other Hardware: None

Operating System: Red Hat Enterprise Linux AS release 4 (Update 4)
Compiler: Intel C++ Compiler 9.1 for Linux (Build 20061105)
Auto Parallel: No
File System: ext3
System State: Multi-user
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill Smartheap 8.0
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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
stacksize set to unlimited prior to run

Base Compiler Invocation
C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA64
401.bzip2: -DSPEC_CPU_LP64
403.mcf: -DSPEC_CPU_LP64
429.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
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Base Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

Base Optimization Flags

C benchmarks:
- fast -IPF\_fp\_relaxed -ansi-alias

C++ benchmarks:
- fast -IPF\_fp\_relaxed -ansi-alias -Wl,-z,muldefs
  /opt/SmartHeap\_8/lib/libsmartheapC64.a
  /opt/SmartHeap\_8/lib/libsmartheap64.a

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast -IPF\_fp\_relaxed
-ansi-alias

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: basepeak = yes

Continued on next page
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Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-anzi-alias -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-anzi-alias -inline-factor=150 -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

483.xalancbmk: Same as 471.omnetpp

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/IPF_intel91_flags.20090715.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/IPF_intel91_flags.20090715.00.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.