SPEC® CINT2006 Result

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Hewlett-Packard Company

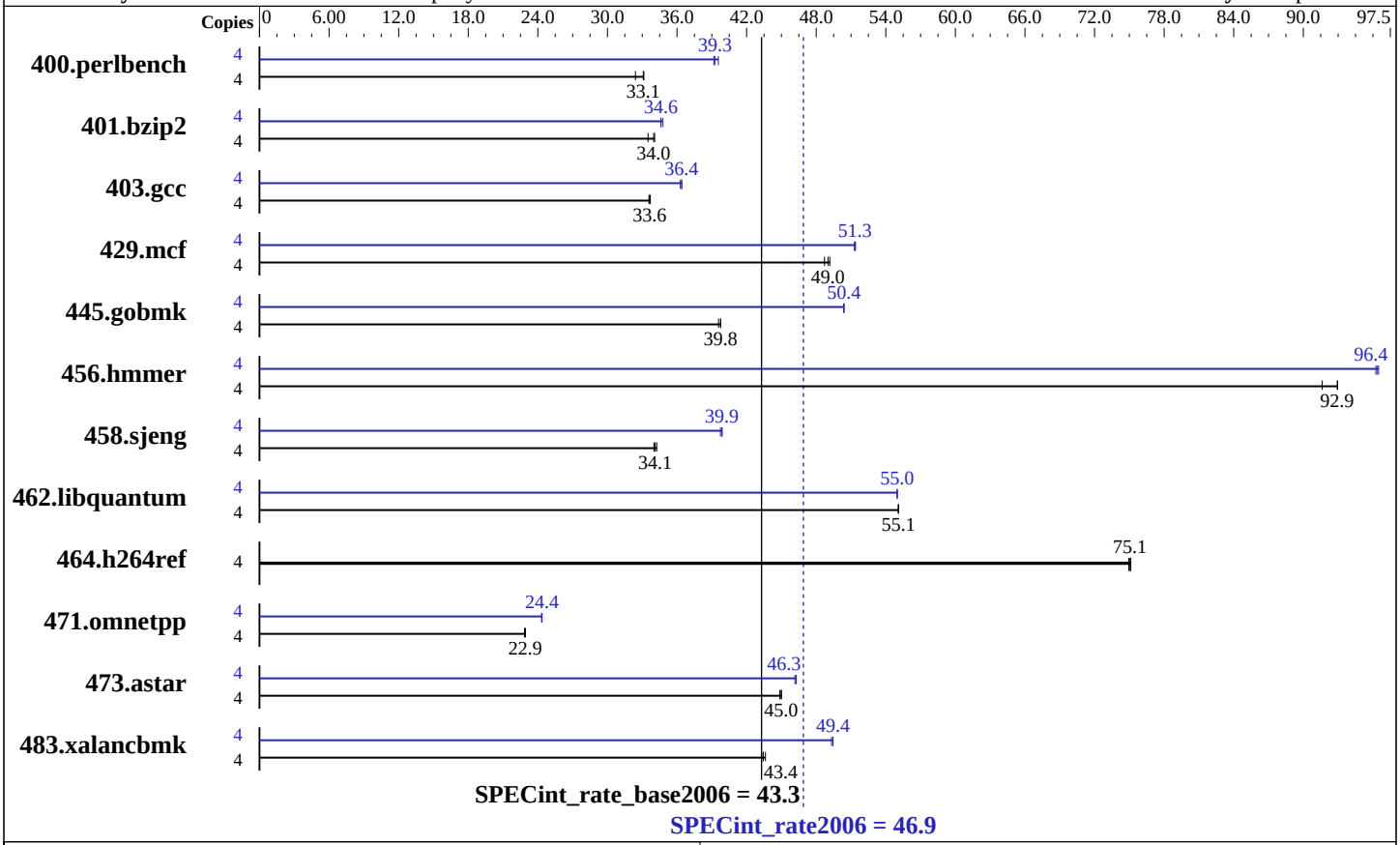
HP Integrity rx3600
(1.4GHz/12MB Dual-Core Intel Itanium 2)

SPECint®_rate2006 = 46.9

SPECint_rate_base2006 = 43.3

CPU2006 license: 03
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Sep-2006
Software Availability: Sep-2006



Hardware

CPU Name:	Dual-Core Intel Itanium 2 9020
CPU Characteristics:	1.4GHz/12MB, 533MHz FSB
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	6 MB I+D on chip per core
Other Cache:	None
Memory:	16 GB (8x2GB DIMMs, AD124A 8-DIMM memory carrier)
Disk Subsystem:	73GB 10K RPM SAS
Other Hardware:	None

Software

Operating System:	HPUX11i-TCOE B.11.23.0609
Compiler:	HP C/aC++ Developer's Bundle C.11.23.12
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill Smartheap 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	1206	32.4	**1181**	**33.1**	1179	33.1	4	998	39.2	**995**	**39.3**	988	39.6
401.bzip2	4	1152	33.5	**1136**	**34.0**	1133	34.1	4	1116	34.6	**1115**	**34.6**	1110	34.8
403.gcc	4	956	33.7	**957**	**33.6**	959	33.6	4	888	36.3	884	36.4	**885**	**36.4**
429.mcf	4	749	48.7	**744**	**49.0**	742	49.2	4	710	51.4	**710**	**51.3**	711	51.3
445.gobmk	4	1060	39.6	1055	39.8	**1055**	**39.8**	4	833	50.4	**833**	**50.4**	833	50.4
456.hmmer	4	407	91.6	401	93.0	**402**	**92.9**	4	**387**	**96.4**	388	96.3	387	96.5
458.sjeng	4	1423	34.0	1413	34.3	**1419**	**34.1**	4	1218	39.7	**1214**	**39.9**	1213	39.9
462.libquantum	4	1505	55.1	**1504**	**55.1**	1504	55.1	4	**1507**	**55.0**	1508	55.0	1507	55.0
464.h264ref	4	1181	74.9	1178	75.1	**1179**	**75.1**	4	1181	74.9	1178	75.1	**1179**	**75.1**
471.omnetpp	4	1094	22.9	1090	22.9	**1090**	**22.9**	4	1028	24.3	**1026**	**24.4**	1026	24.4
473.astar	4	626	44.9	**624**	**45.0**	624	45.0	4	608	46.2	607	46.3	**607**	**46.3**
483.xalancbmk	4	636	43.4	633	43.6	**635**	**43.4**	4	560	49.3	558	49.5	**558**	**49.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
The system had the September 2006 HP-UX 11i v2 Technical Computing
Operating Environment (TCOE) and compilers installed, along with
the following patches:

PHSS_34858  linker + fdp cumulative patch
PHSS_34853  Math Library Cumulative Patch
PHSS_34854  Integrity Unwind Library
PHSS_34855  HP C Compiler (A.06.12)
PHSS_34856  aC++ Compiler (A.06.12)
PHSS_34857  u2comp/be/plugin library patch
PHSS_34395  FORTRAN I/O Library [libIO77]
PHSS_34397  FORTRAN Intrinsics [libF90 B.11.23.17]
PHSS_34399  Fortran Product Patch, v3.1 to v3.1.1
PHKL_34020  Perfmon enhancements and Itanium Dual-Core

The following kernel tunables were set, in addition to the defaults
set by the Technical Computing OE:

dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

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Base Compiler Invocation

C benchmarks:
```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:
```
/opt/aCC/bin/aCC -Aa
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Base Optimization Flags

C benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

C++ benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a
```

Peak Compiler Invocation

C benchmarks:
```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:
```
/opt/aCC/bin/aCC -Aa
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Peak Optimization Flags

C benchmarks:

400.perlbench: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M -Wl,-N

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Odataprefetch=direct

456.hmmer: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M

458.sjeng: Same as 445.gobmk

462.libquantum: Same as 456.hmmer

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

473.astar: +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

483.xalancbmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.xml

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For other inquiries, please contact webmaster@spec.org.

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