SPEC® CFP2006 Result

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IBM Corporation

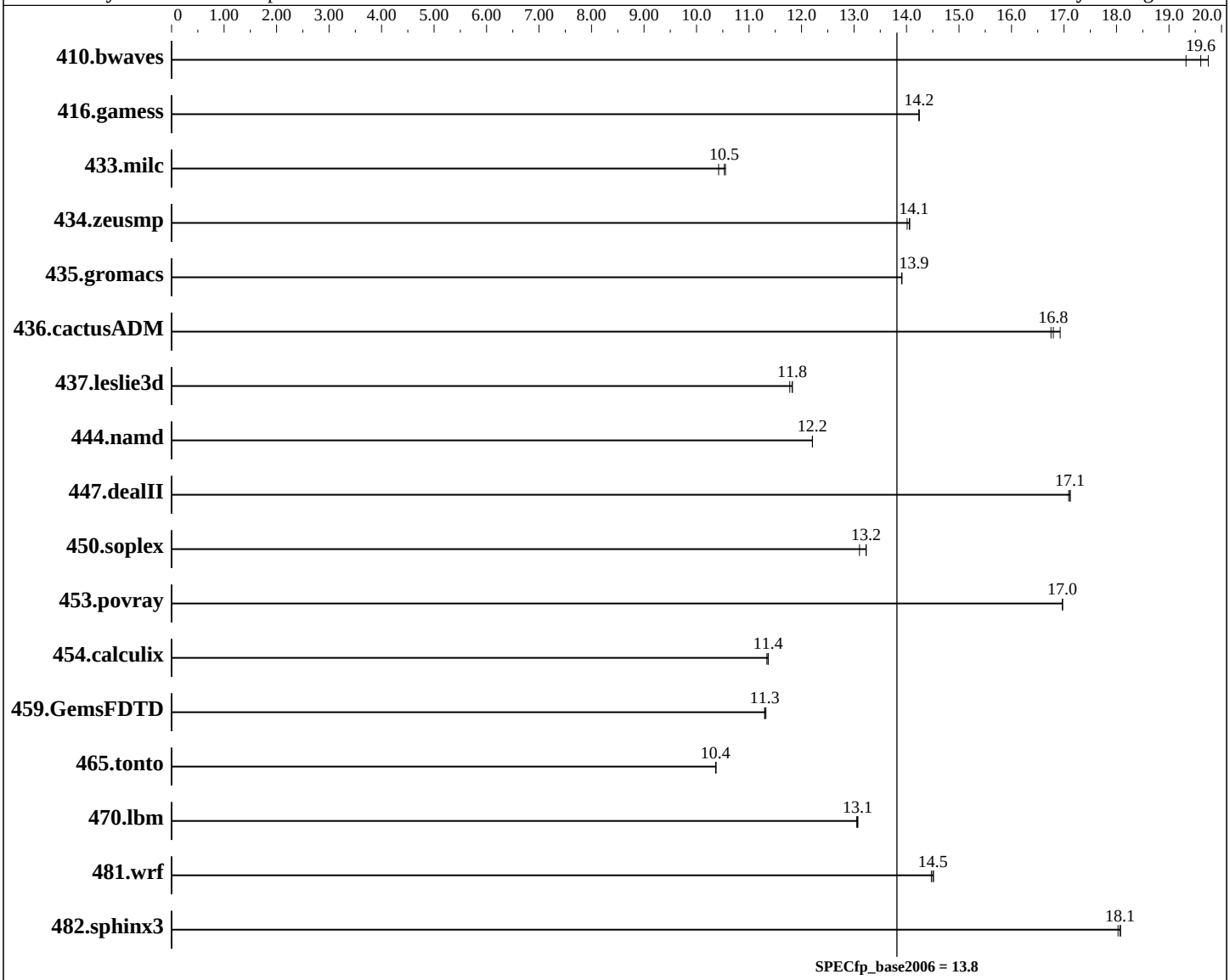
IBM System x3250 (Intel Xeon X3220)

SPECfp®2006 = Not Run

SPECfp_base2006 = 13.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	1066MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Smart Heap Library, Version 8

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	703	19.3	688	19.7	**693**	**19.6**						
416.gamess	**1375**	**14.2**	1375	14.2	1375	14.2						
433.milc	881	10.4	870	10.6	**872**	**10.5**						
434.zeusmp	649	14.0	**648**	**14.1**	647	14.1						
435.gromacs	513	13.9	**513**	**13.9**	513	13.9						
436.cactusADM	713	16.8	706	16.9	**711**	**16.8**						
437.leslie3d	798	11.8	**795**	**11.8**	795	11.8						
444.namd	657	12.2	**657**	**12.2**	657	12.2						
447.dealII	669	17.1	**668**	**17.1**	668	17.1						
450.soplex	636	13.1	630	13.2	**630**	**13.2**						
453.povray	314	17.0	**314**	**17.0**	313	17.0						
454.calculix	728	11.3	726	11.4	**726**	**11.4**						
459.GemsFDTD	939	11.3	937	11.3	**939**	**11.3**						
465.tonto	949	10.4	**949**	**10.4**	949	10.4						
470.lbm	1053	13.1	**1051**	**13.1**	1051	13.1						
481.wrf	772	14.5	770	14.5	**770**	**14.5**						
482.sphinx3	1081	18.0	1078	18.1	**1079**	**18.1**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

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