SPEC® CINT2006 Result

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IBM Corporation

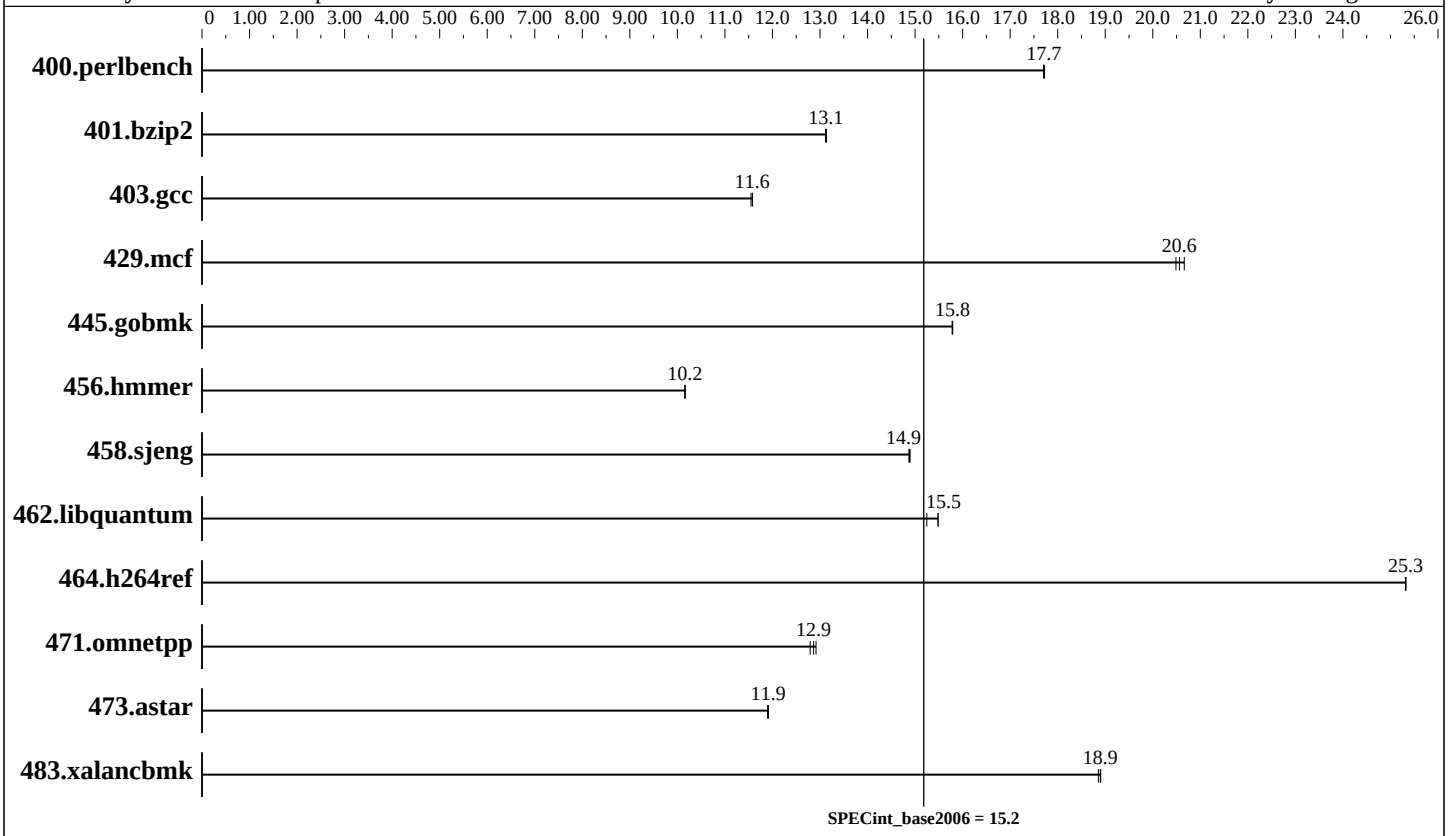
IBM System x3250 (Intel Xeon X3220)

SPECint®2006 = Not Run

SPECint_base2006 = 15.2

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	1066MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	552	17.7	551	17.7	**552**	**17.7**						
401.bzip2	736	13.1	735	13.1	**735**	**13.1**						
403.gcc	697	11.5	695	11.6	**695**	**11.6**						
429.mcf	445	20.5	**444**	**20.6**	441	20.7						
445.gobmk	665	15.8	**664**	**15.8**	664	15.8						
456.hmmer	918	10.2	918	10.2	**918**	**10.2**						
458.sjeng	814	14.9	812	14.9	**813**	**14.9**						
462.libquantum	1359	15.2	**1338**	**15.5**	1338	15.5						
464.h264ref	874	25.3	874	25.3	**874**	**25.3**						
471.omnetpp	489	12.8	484	12.9	**486**	**12.9**						
473.astar	590	11.9	589	11.9	**589**	**11.9**						
483.xalancbmk	366	18.9	**365**	**18.9**	365	18.9						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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