SPEC® CINT2006 Result

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IBM Corporation

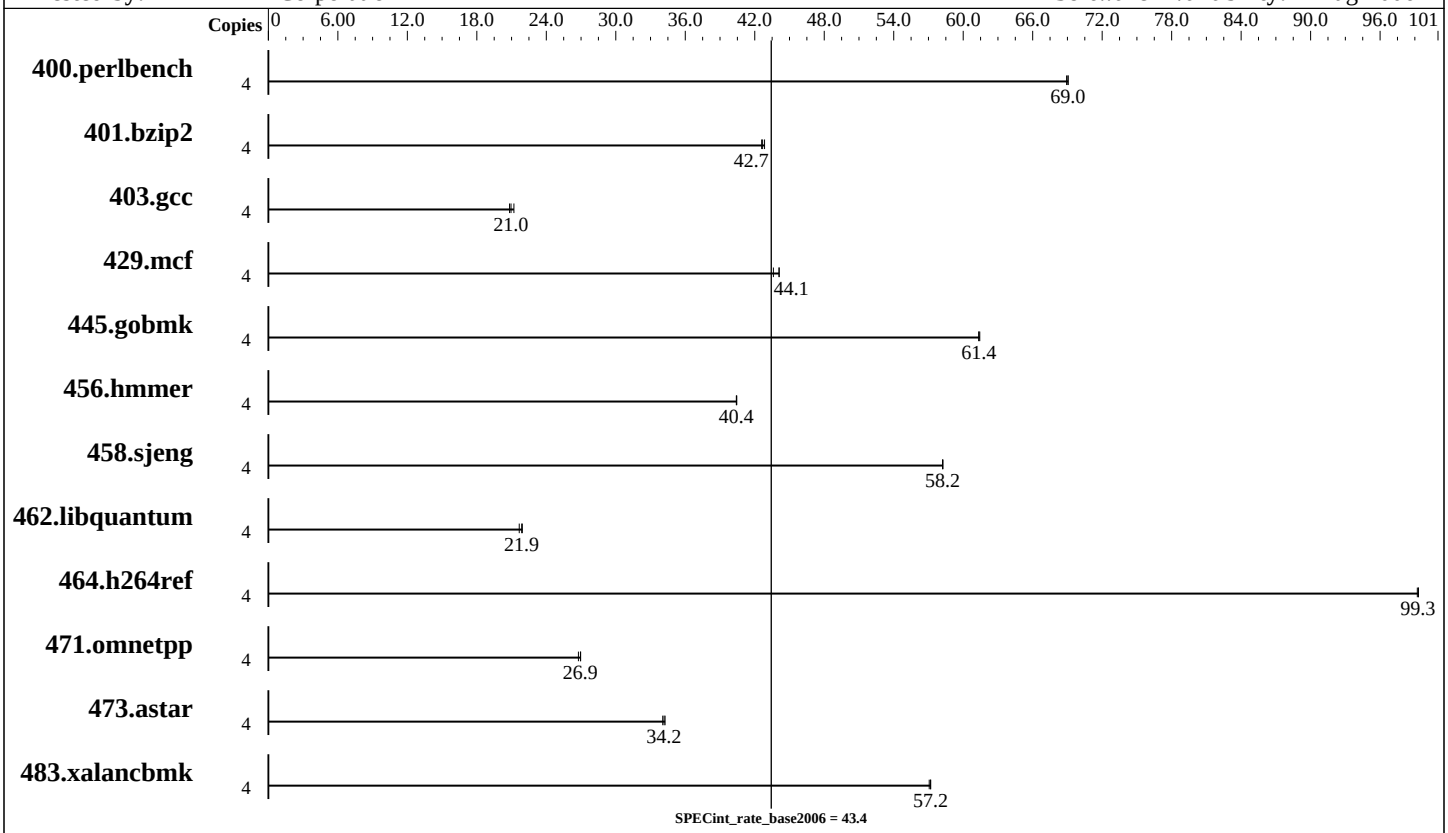
IBM System x3200 (Intel Xeon X3220)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 43.4

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	1066MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem:	1 x 80 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	567	68.9	566	69.1	**566**	**69.0**							
401.bzip2	4	**904**	**42.7**	906	42.6	901	42.8							
403.gcc	4	**1537**	**21.0**	1519	21.2	1546	20.8							
429.mcf	4	837	43.6	**827**	**44.1**	827	44.1							
445.gobmk	4	684	61.3	**684**	**61.4**	683	61.4							
456.hmmer	4	923	40.4	923	40.4	**923**	**40.4**							
458.sjeng	4	**831**	**58.2**	831	58.2	831	58.2							
462.libquantum	4	3826	21.7	3780	21.9	**3789**	**21.9**							
464.h264ref	4	891	99.3	**891**	**99.3**	892	99.2							
471.omnetpp	4	934	26.8	927	27.0	**928**	**26.9**							
473.astar	4	825	34.1	820	34.3	**822**	**34.2**							
483.xalancbmk	4	484	57.1	**483**	**57.2**	483	57.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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