SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

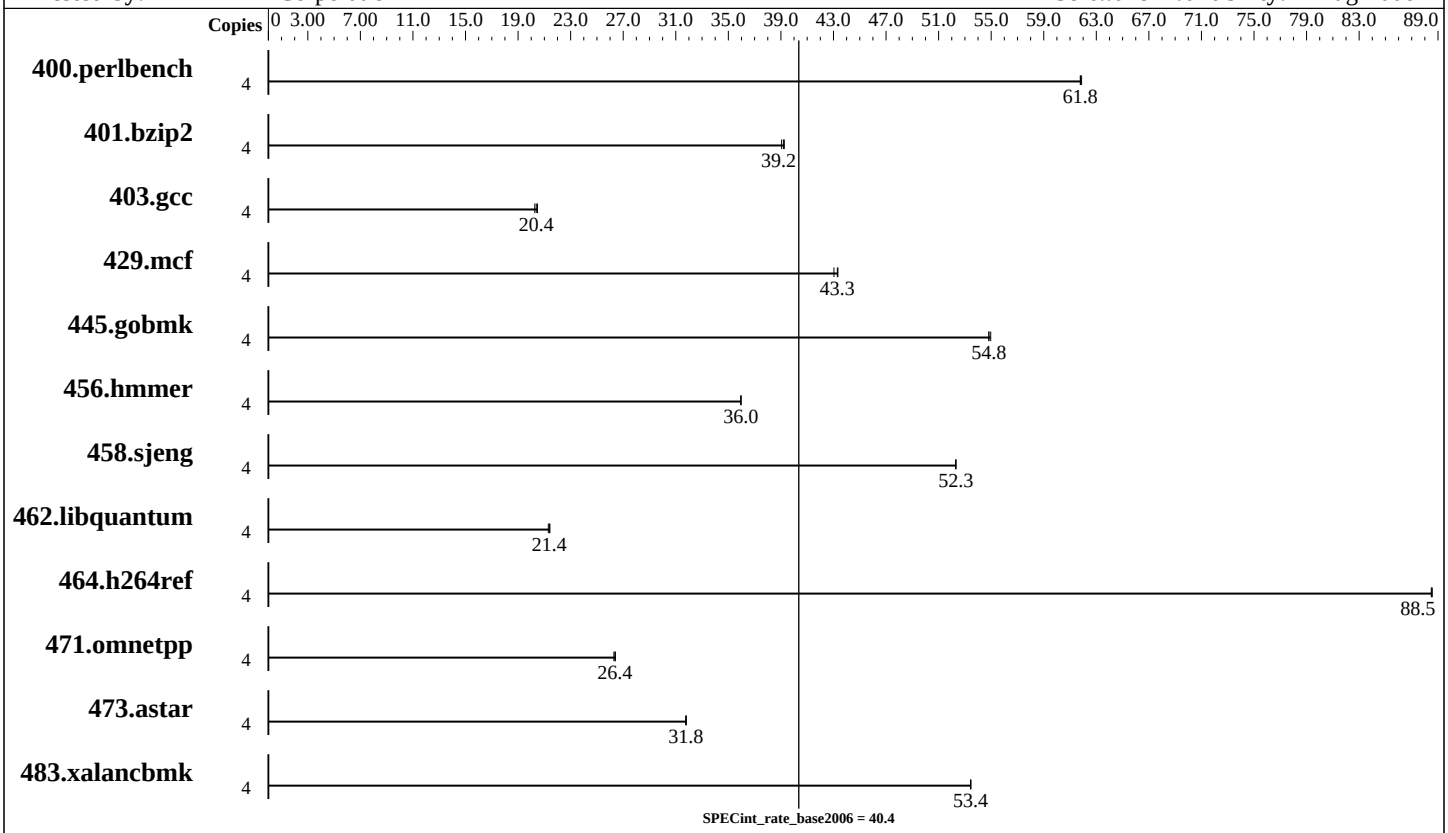
IBM System x3200 (Intel Xeon X3210)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 40.4

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X3210
CPU Characteristics:	1066MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem:	1 x 80 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3200 (Intel Xeon X3210)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 40.4

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	633	61.8	632	61.9	**632**	**61.8**							
401.bzip2	4	988	39.1	**985**	**39.2**	983	39.3							
403.gcc	4	1588	20.3	1573	20.5	**1580**	**20.4**							
429.mcf	4	848	43.0	842	43.3	**843**	**43.3**							
445.gobmk	4	**765**	**54.8**	765	54.8	763	55.0							
456.hmmer	4	1038	36.0	1038	36.0	**1038**	**36.0**							
458.sjeng	4	**925**	**52.3**	925	52.3	925	52.3							
462.libquantum	4	3891	21.3	3869	21.4	**3876**	**21.4**							
464.h264ref	4	**1000**	**88.5**	1000	88.6	1000	88.5							
471.omnetpp	4	951	26.3	947	26.4	**948**	**26.4**							
473.astar	4	884	31.8	883	31.8	**884**	**31.8**							
483.xalancbmk	4	517	53.4	516	53.5	**516**	**53.4**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:16:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 February 2007.