SPEC® CINT2006 Result

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IBM Corporation

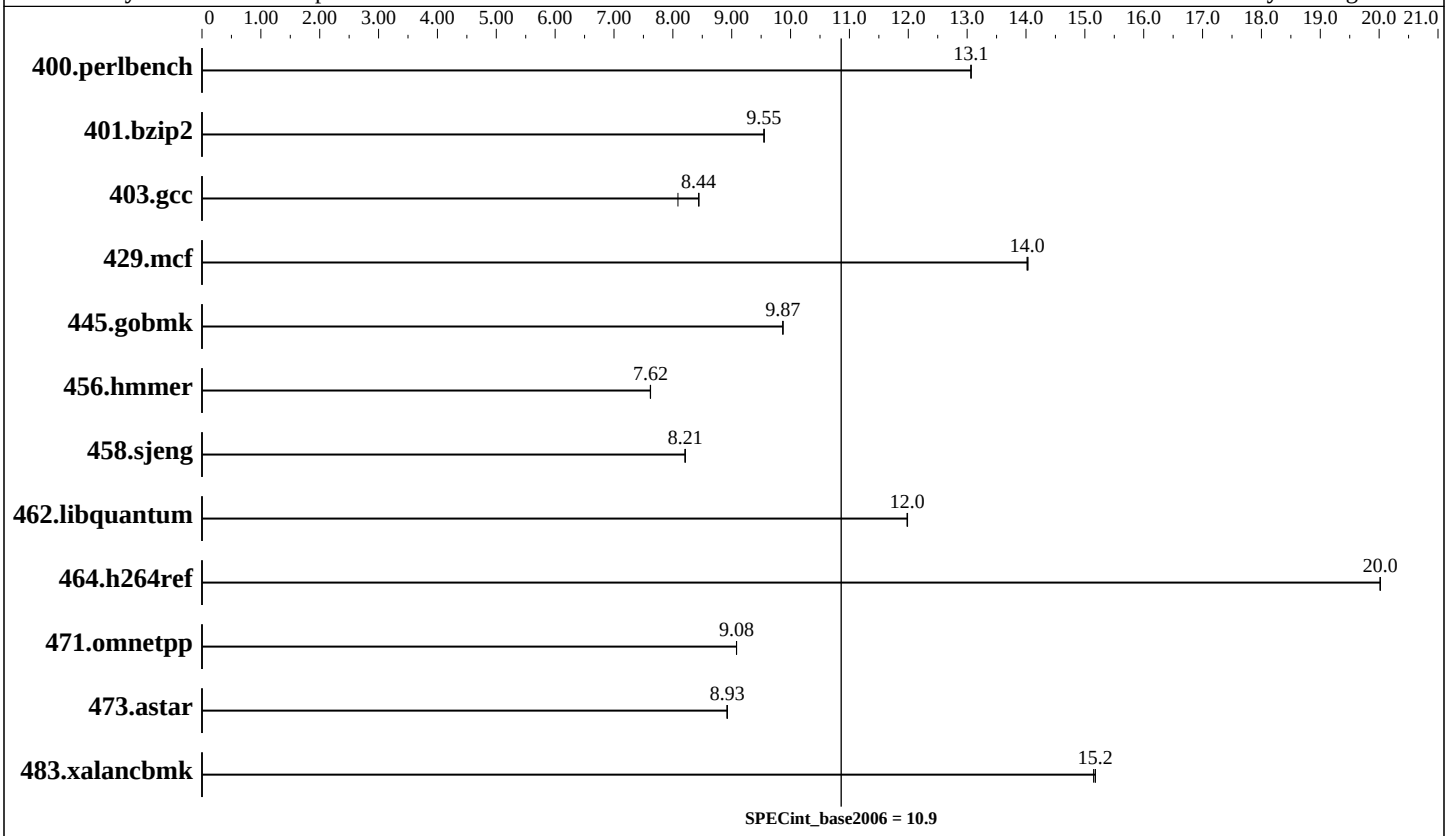
IBM System x3800 (Intel Xeon 7150N)

SPECint®2006 = Not Run

SPECint_base2006 = 10.9

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 7150N
CPU Characteristics:	667 MHz bus
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**748**	**13.1**	748	13.1	748	13.1						
401.bzip2	**1010**	**9.55**	1010	9.55	1011	9.55						
403.gcc	995	8.09	**954**	**8.44**	953	8.45						
429.mcf	651	14.0	**650**	**14.0**	650	14.0						
445.gobmk	**1063**	**9.87**	1063	9.87	1063	9.87						
456.hmmer	**1224**	**7.62**	1225	7.62	1224	7.62						
458.sjeng	**1474**	**8.21**	1474	8.21	1474	8.21						
462.libquantum	1730	12.0	**1729**	**12.0**	1728	12.0						
464.h264ref	1106	20.0	**1105**	**20.0**	1105	20.0						
471.omnetpp	688	9.08	**688**	**9.08**	688	9.08						
473.astar	787	8.92	786	8.93	**786**	**8.93**						
483.xalancbmk	**455**	**15.2**	455	15.2	456	15.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml`

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