



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint[®]_rate2006 = 44.4

PowerEdge 860 (Intel Xeon X3220, 2.40 GHz)

SPECint_rate_base2006 = 43.2

CPU2006 license: 55

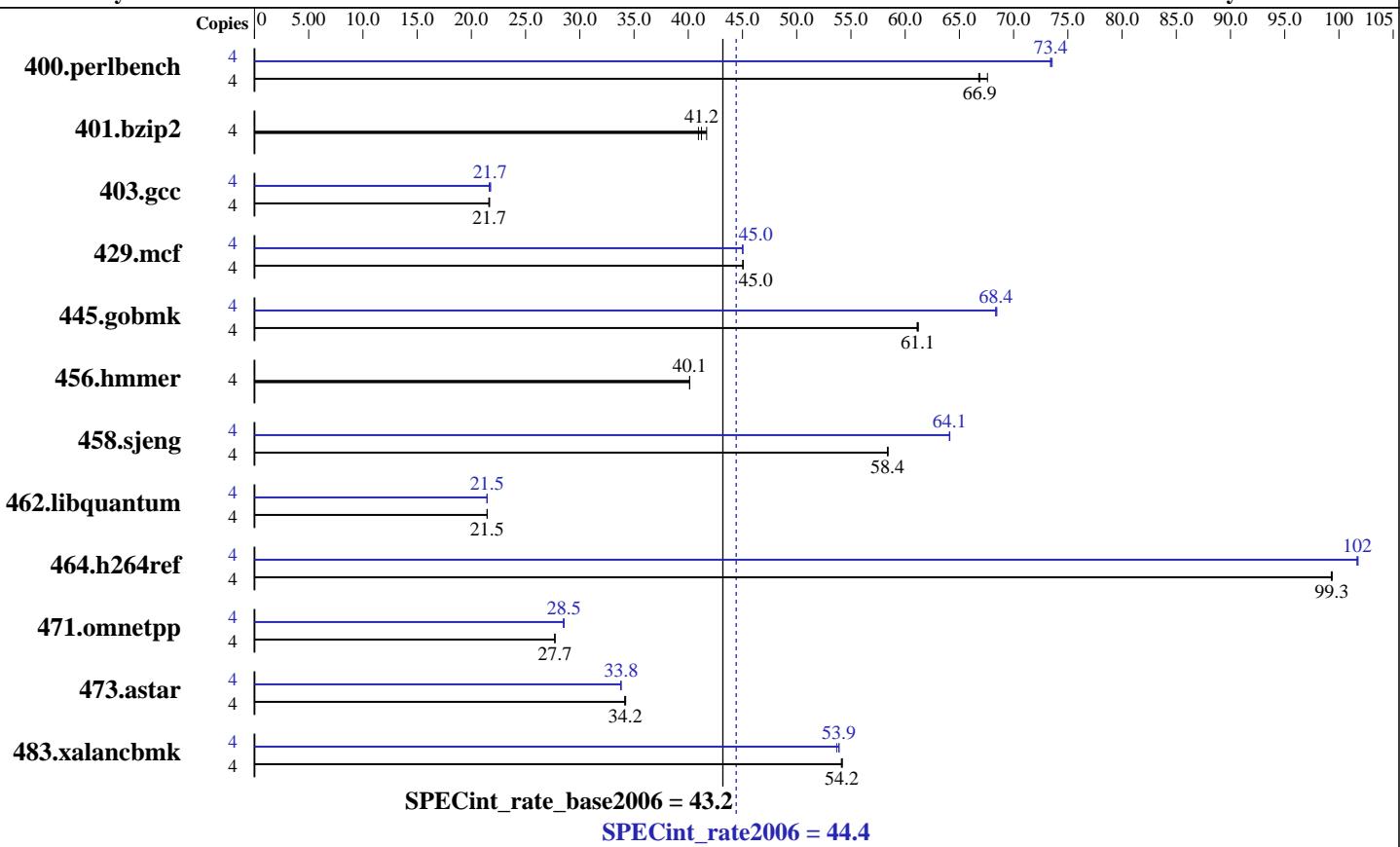
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jan-2007

Hardware Availability: Jan-2007

Software Availability: Oct-2006



Hardware

CPU Name: Intel Xeon X3220
CPU Characteristics: 1066MHz system bus
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB 667MHz ECC DDR2 SDRAM)
Disk Subsystem: 1 x 73GB SAS 10000 RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 x64 Enterprise Edition
Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z)
Microsoft Visual Studio 2005 (8.0.50727.42)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 44.4

PowerEdge 860 (Intel Xeon X3220, 2.40 GHz)

SPECint_rate_base2006 = 43.2

CPU2006 license: 55

Test date: Jan-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2007

Tested by: Dell Inc.

Software Availability: Oct-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	578	67.6	584	66.9	585	66.8	4	532	73.4	531	73.6	532	73.4
401.bzip2	4	936	41.2	926	41.7	943	40.9	4	936	41.2	926	41.7	943	40.9
403.gcc	4	1487	21.7	1485	21.7	1490	21.6	4	1483	21.7	1488	21.6	1478	21.8
429.mcf	4	810	45.0	810	45.0	810	45.1	4	810	45.0	810	45.0	810	45.0
445.gobmk	4	687	61.1	685	61.2	686	61.1	4	613	68.5	614	68.4	614	68.3
456.hammer	4	930	40.1	930	40.1	930	40.1	4	930	40.1	930	40.1	930	40.1
458.sjeng	4	829	58.4	828	58.4	829	58.4	4	755	64.1	755	64.1	755	64.1
462.libquantum	4	3864	21.4	3863	21.5	3861	21.5	4	3866	21.4	3862	21.5	3862	21.5
464.h264ref	4	892	99.3	891	99.4	891	99.3	4	870	102	871	102	870	102
471.omnetpp	4	902	27.7	903	27.7	902	27.7	4	876	28.5	876	28.5	877	28.5
473.astar	4	820	34.2	823	34.1	821	34.2	4	831	33.8	831	33.8	831	33.8
483.xalancbmk	4	510	54.2	509	54.2	510	54.1	4	512	53.9	512	53.9	514	53.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Adjacent Sector Prefetch disabled in BIOS, default enabled
32-bit binaries were built on Windows XP Professional x64 Edition

Base Compiler Invocation

C benchmarks:
 icl -Qc99

C++ benchmarks:
 icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
 -fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 860 (Intel Xeon X3220, 2.40 GHz)

SPECint_rate2006 = 44.4

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jan-2007

Hardware Availability: Jan-2007

Software Availability: Oct-2006

Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32M.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qc99
```

C++ benchmarks:

```
icl
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast  
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: basepeak = yes
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: ONESTEP -fast /F512000000 shlw32M.lib  
-link /FORCE:MULTIPLE
```

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000  
shlw32M.lib -link /FORCE:MULTIPLE
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 44.4

PowerEdge 860 (Intel Xeon X3220, 2.40 GHz)

SPECint_rate_base2006 = 43.2

CPU2006 license: 55

Test date: Jan-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2007

Tested by: Dell Inc.

Software Availability: Oct-2006

Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
-Qcxx_features /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: ONESTEP -fast -Qcxx_features /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.20090715.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.20090715.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 10:58:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 February 2007.