Dell Inc.

PowerEdge 840 (Intel Xeon X3210, 2.13 GHz)

SPECint®_rate2006 = 41.0
SPECint_rate_base2006 = 39.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

SPECint_rate2006 = 41.0
SPECint_rate_base2006 = 39.8

Hardware

CPU Name: Intel Xeon X3210
CPU Characteristics: 1066MHz system bus
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per core, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB 533MHz ECC DDR2 SDRAM)
Disk Subsystem: 1 x 73GB SAS 10000 RPM
Other Hardware: None

Software

Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z)
Microsoft Visual Studio 2005 (8.0.50727.42)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0
**SPEC CINT2006 Result**

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Test date: Jan-2007
Hardware Availability: Jan-2007
Tested by: Dell Inc.
Software Availability: Oct-2006

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Base Copies</th>
<th>Seconds</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Peak Copies</th>
<th>Peak Seconds</th>
<th>Peak Seconds</th>
<th>Peak Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>4</td>
<td>641</td>
<td>60.9</td>
<td>642</td>
<td>60.9</td>
<td>641</td>
<td>60.9</td>
<td></td>
<td>4</td>
<td>582</td>
<td>67.2</td>
<td>582</td>
<td>67.1</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td>994</td>
<td>38.8</td>
<td>997</td>
<td>38.7</td>
<td>994</td>
<td>38.8</td>
<td></td>
<td>4</td>
<td>994</td>
<td>38.8</td>
<td>997</td>
<td>38.7</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>1683</td>
<td>19.1</td>
<td>1672</td>
<td>19.3</td>
<td>1672</td>
<td>19.3</td>
<td></td>
<td>4</td>
<td>1659</td>
<td>19.4</td>
<td>1653</td>
<td>19.5</td>
</tr>
<tr>
<td>429.mcf</td>
<td>4</td>
<td>831</td>
<td>43.9</td>
<td>829</td>
<td>44.0</td>
<td>830</td>
<td>43.9</td>
<td></td>
<td>4</td>
<td>830</td>
<td>43.9</td>
<td>829</td>
<td>44.0</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>4</td>
<td>767</td>
<td>54.7</td>
<td>766</td>
<td>54.8</td>
<td>767</td>
<td>54.7</td>
<td></td>
<td>4</td>
<td>685</td>
<td>61.2</td>
<td>685</td>
<td>61.2</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>1046</td>
<td>35.7</td>
<td>1046</td>
<td>35.7</td>
<td>1046</td>
<td>35.7</td>
<td></td>
<td>4</td>
<td>1046</td>
<td>35.7</td>
<td>1046</td>
<td>35.7</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>920</td>
<td>52.6</td>
<td>920</td>
<td>52.6</td>
<td>921</td>
<td>52.6</td>
<td></td>
<td>4</td>
<td>839</td>
<td>57.7</td>
<td>838</td>
<td>57.8</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td>4095</td>
<td>20.2</td>
<td>4061</td>
<td>20.4</td>
<td>4060</td>
<td>20.4</td>
<td></td>
<td>4</td>
<td>4083</td>
<td>20.3</td>
<td>4049</td>
<td>20.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>4</td>
<td>997</td>
<td>88.8</td>
<td>999</td>
<td>88.8</td>
<td>997</td>
<td>88.8</td>
<td></td>
<td>4</td>
<td>973</td>
<td>91.0</td>
<td>973</td>
<td>91.0</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>4</td>
<td>946</td>
<td>26.4</td>
<td>943</td>
<td>26.5</td>
<td>944</td>
<td>26.5</td>
<td></td>
<td>4</td>
<td>916</td>
<td>27.3</td>
<td>913</td>
<td>27.4</td>
</tr>
<tr>
<td>473.astar</td>
<td>4</td>
<td>886</td>
<td>31.7</td>
<td>884</td>
<td>31.8</td>
<td>884</td>
<td>31.8</td>
<td></td>
<td>4</td>
<td>894</td>
<td>31.4</td>
<td>893</td>
<td>31.4</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>4</td>
<td>548</td>
<td>50.4</td>
<td>548</td>
<td>50.3</td>
<td>548</td>
<td>50.4</td>
<td></td>
<td>4</td>
<td>550</td>
<td>50.1</td>
<td>551</td>
<td>50.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**General Notes**

Adjacent Sector Prefetch disabled in BIOS, default enabled
32-bit binaries were built on Windows XP Professional x64 Edition

**Base Compiler Invocation**

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

**Base Portability Flags**

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

**Base Optimization Flags**

C benchmarks:
-fast /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

Continued on next page.
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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32M.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -DllCaloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc99
C++ benchmarks:
icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:
400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE
401.bzip2: basepeak = yes
403.gcc: Same as 400.perlbench
429.mcf: ONESTEP -fast /F512000000 shlw32M.lib
- link /FORCE:MULTIPLE
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32M.lib - link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: ONESTEP -fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.20090715.html

You can also download the XML flags source by saving the following link: http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.20090715.xml