Dell Inc.

PowerEdge 860 (Intel Xeon X3210, 2.13 GHz)

**SPECint**_rate2006 = 40.6

**SPECint_rate_base2006 = 39.5**

**Dell Inc.**

**Specint®_rate2006 = 40.6**

**SPECint_rate_base2006 = 39.5**

**Hardware**

- CPU Name: Intel Xeon X3210
- CPU Characteristics: 1066MHz system bus
- CPU MHz: 2133
- FPU: Integrated
- CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
- L3 Cache: None
- Other Cache: None
- Memory: 8 GB (4x2 GB 533MHz ECC DDR2 SDRAM)
- Disk Subsystem: 1 x 73GB SAS 10000 RPM
- Other Hardware: None

**Software**

- Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z)
- Auto Parallel: No
- File System: NTFS
- System State: Default
- Base Pointers: 32-bit
- Peak Pointers: 32-bit
- Other Software: MicroQuill SmartHeap Library 8.0
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CPU2006 license: 55
Test sponsor: Dell Inc.
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Results Table

<table>
<thead>
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<th>Ratio</th>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
Adjacent Sector Prefetch disabled in BIOS, default enabled
32-bit binaries were built on Windows XP Professional x64 Edition

Base Compiler Invocation
C benchmarks:
icl -Qc99
C++ benchmarks:
icl

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags
C benchmarks:
-fast /F512000000 shlW32M.lib

Continued on next page
SPEC CINT2006 Result

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Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Oct-2006

Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32M.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc99
C++ benchmarks:
icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:
400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE
401.bzip2: basepeak = yes
403.gcc: Same as 400.perlbench
429.mcf: ONESTEP -fast /F512000000 shlw32M.lib
  -link /FORCE:MULTIPLE
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
  shlw32M.lib -link /FORCE:MULTIPLE
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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
-Qcxx_features /F512000000 shlw32M.lib
- link /FORCE:_MULTIPLE
473.astar: Same as 471.omnetpp
483.xalancbmk: ONESTEP -fast -Qcxx_features /F512000000 shlw32M.lib
- link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at 

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.20090715.xml

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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