# SPEC® CINT2006 Result

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## Hewlett-Packard Company

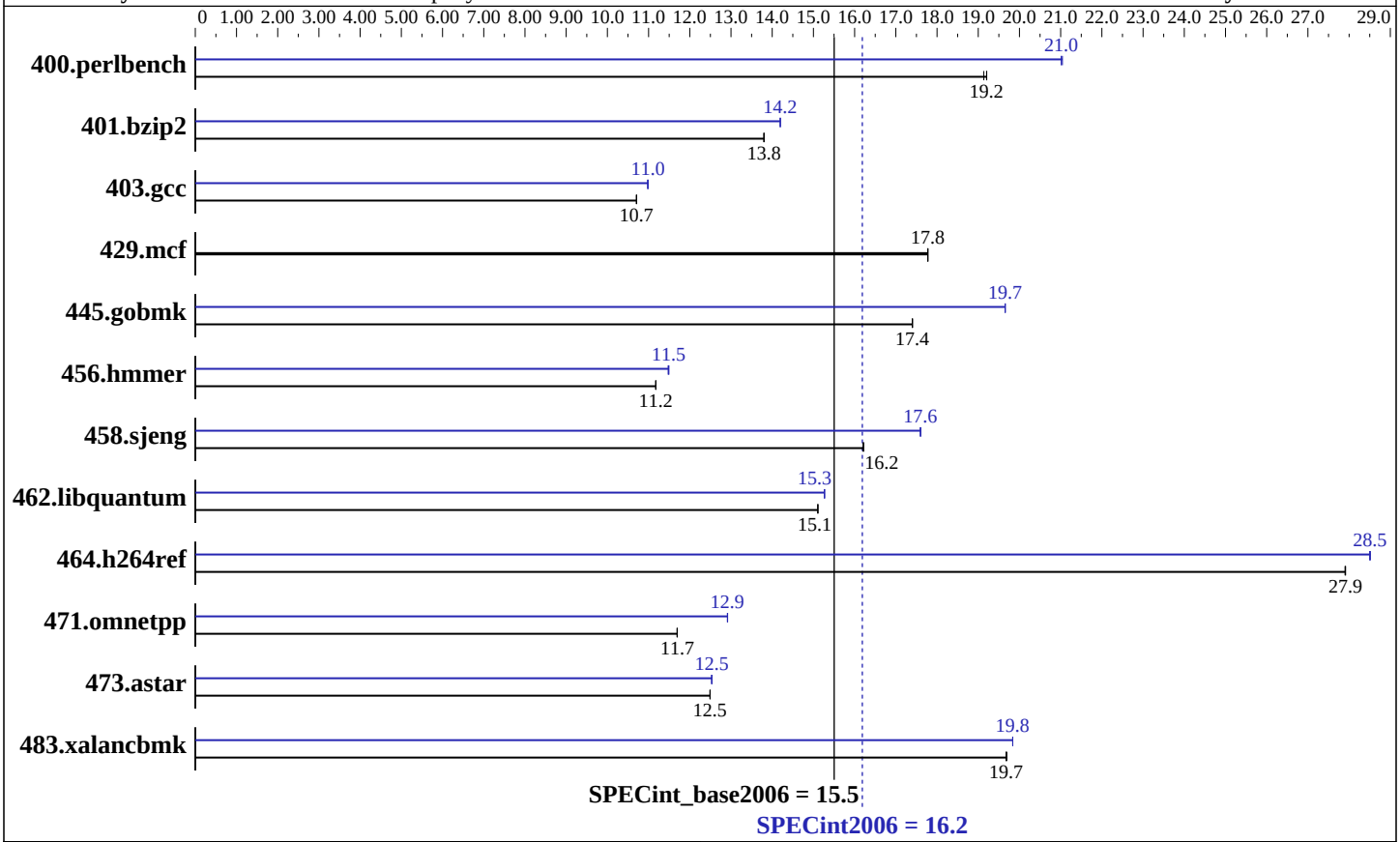
ProLiant ML370 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint®2006 = 16.2

SPECint_base2006 = 15.5

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Feb-2007
**Hardware Availability:** Jan-2007
**Software Availability:** Nov-2006




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5355 |
| CPU Characteristics: | 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus |
| CPU MHz: | 2666 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per chip, 4 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 16 GB (8x2 GB PC2-5300F CL5) |
| Disk Subsystem: | 2x72 GB 10 K SAS |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise X64 Edition |
| Compiler: | Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z<br>Package ID: W_CC_P_9.1.020<br>Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32-bit |
| Other Software: | MicroQuill SmartHeap Library 8.0 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 511 | 19.1 | 509 | 19.2 | **509** | **19.2** | 465 | 21.0 | **465** | **21.0** | 464 | 21.0 |
| 401.bzip2 | **699** | **13.8** | 699 | 13.8 | 699 | 13.8 | **680** | **14.2** | 680 | 14.2 | 680 | 14.2 |
| 403.gcc | **752** | **10.7** | 752 | 10.7 | 752 | 10.7 | 733 | 11.0 | **733** | **11.0** | 733 | 11.0 |
| 429.mcf | 513 | 17.8 | **513** | **17.8** | 513 | 17.8 | 513 | 17.8 | **513** | **17.8** | 513 | 17.8 |
| 445.gobmk | 603 | 17.4 | 603 | 17.4 | **603** | **17.4** | 534 | 19.7 | **534** | **19.7** | 534 | 19.7 |
| 456.hmmer | 835 | 11.2 | 835 | 11.2 | **835** | **11.2** | 813 | 11.5 | **812** | **11.5** | 812 | 11.5 |
| 458.sjeng | **746** | **16.2** | 746 | 16.2 | 747 | 16.2 | 687 | 17.6 | 688 | 17.6 | **688** | **17.6** |
| 462.libquantum | **1371** | **15.1** | 1371 | 15.1 | 1372 | 15.1 | 1357 | 15.3 | 1357 | 15.3 | **1357** | **15.3** |
| 464.h264ref | 793 | 27.9 | **793** | **27.9** | 793 | 27.9 | 776 | 28.5 | **776** | **28.5** | 776 | 28.5 |
| 471.omnetpp | 535 | 11.7 | 534 | 11.7 | **534** | **11.7** | 484 | 12.9 | **484** | **12.9** | 484 | 12.9 |
| 473.astar | 562 | 12.5 | **562** | **12.5** | 562 | 12.5 | 560 | 12.5 | **560** | **12.5** | 560 | 12.5 |
| 483.xalancbmk | 350 | 19.7 | **351** | **19.7** | 351 | 19.7 | 348 | 19.8 | 348 | 19.8 | **348** | **19.8** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

## Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

Continued on next page

## Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

Continued on next page

## Peak Optimization Flags (Continued)

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features`
`/F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml`


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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