# SPEC® CINT2006 Result

## Hewlett-Packard Company

### ProLiant ML370 G5 (2.66 GHz, Intel Xeon processor X5355)

| SPECint_rate2006 | 45.8 |
| SPECint_rate_base2006 | 44.4 |

| CPU2006 license: 3 | Test date: Feb-2007 |
| Test sponsor: Hewlett-Packard Company | Hardware Availability: Jan-2007 |
| Tested by: Hewlett-Packard Company | Software Availability: Nov-2006 |

### Hardware

- **CPU Name:** Intel Xeon X5355
- **CPU Characteristics:** 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
- **CPU MHz:** 2666
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 16 GB (8x2 GB PC2-5300F CL5)
- **Disk Subsystem:** 2x72 GB 10 K SAS
- **Other Hardware:** None

### Software

- **Operating System:** Windows Server 2003 Enterprise X64 Edition
- **Compiler:** Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
  - Package ID: W_CC_P_9.1.020
  - Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** MicroQuill SmartHeap Library 8.0

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### Benchmarks

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>41.4</td>
</tr>
<tr>
<td>bzip2</td>
<td>40.6</td>
</tr>
<tr>
<td>gcc</td>
<td>39.8</td>
</tr>
<tr>
<td>mcf</td>
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</tr>
<tr>
<td>gobmk</td>
<td>74.4</td>
</tr>
<tr>
<td>hmer</td>
<td>66.1</td>
</tr>
<tr>
<td>sjeng</td>
<td>63.5</td>
</tr>
<tr>
<td>libquantum</td>
<td>19.2</td>
</tr>
<tr>
<td>h264ref</td>
<td>108</td>
</tr>
<tr>
<td>omnetpp</td>
<td>29.1</td>
</tr>
<tr>
<td>astar</td>
<td>35.4</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>57.3</td>
</tr>
</tbody>
</table>

### Copy Counts

- 400.perlbench: 4 copies
- 401.bzip2: 4 copies
- 403.gcc: 4 copies
- 429.mcf: 4 copies
- 445.gobmk: 4 copies
- 456.hhmer: 4 copies
- 458.sjeng: 4 copies
- 462.libquantum: 4 copies
- 464.h264ref: 4 copies
- 471.omnetpp: 4 copies
- 473.astar: 4 copies
- 483.xalancbmk: 4 copies

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Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Hewlett-Packard Company
ProLiant ML370 G5
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SPECint_rate2006 = 45.8
SPECint_rate_base2006 = 44.4

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
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<td>73.8</td>
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<td>401.bzip2</td>
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<td>950</td>
<td>40.6</td>
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<td>403.gcc</td>
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<td>1423</td>
<td>22.6</td>
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<td>483.xalancbmk</td>
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<td>481</td>
<td>57.4</td>
<td>482</td>
<td>57.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags
C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTCTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
  shlw32m.lib -link /FORCE:MULTIPLE
  401.bzip2: Same as 400.perlbench
  403.gcc: Same as 400.perlbench
  429.mcf: basepeak = yes
  445.gobmk: Same as 400.perlbench
  456.hmmer: Same as 400.perlbench
  458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
- Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For other inquiries, please contact webmaster@spec.org.

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