SPEC® CFP2006 Result

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Hewlett-Packard Company

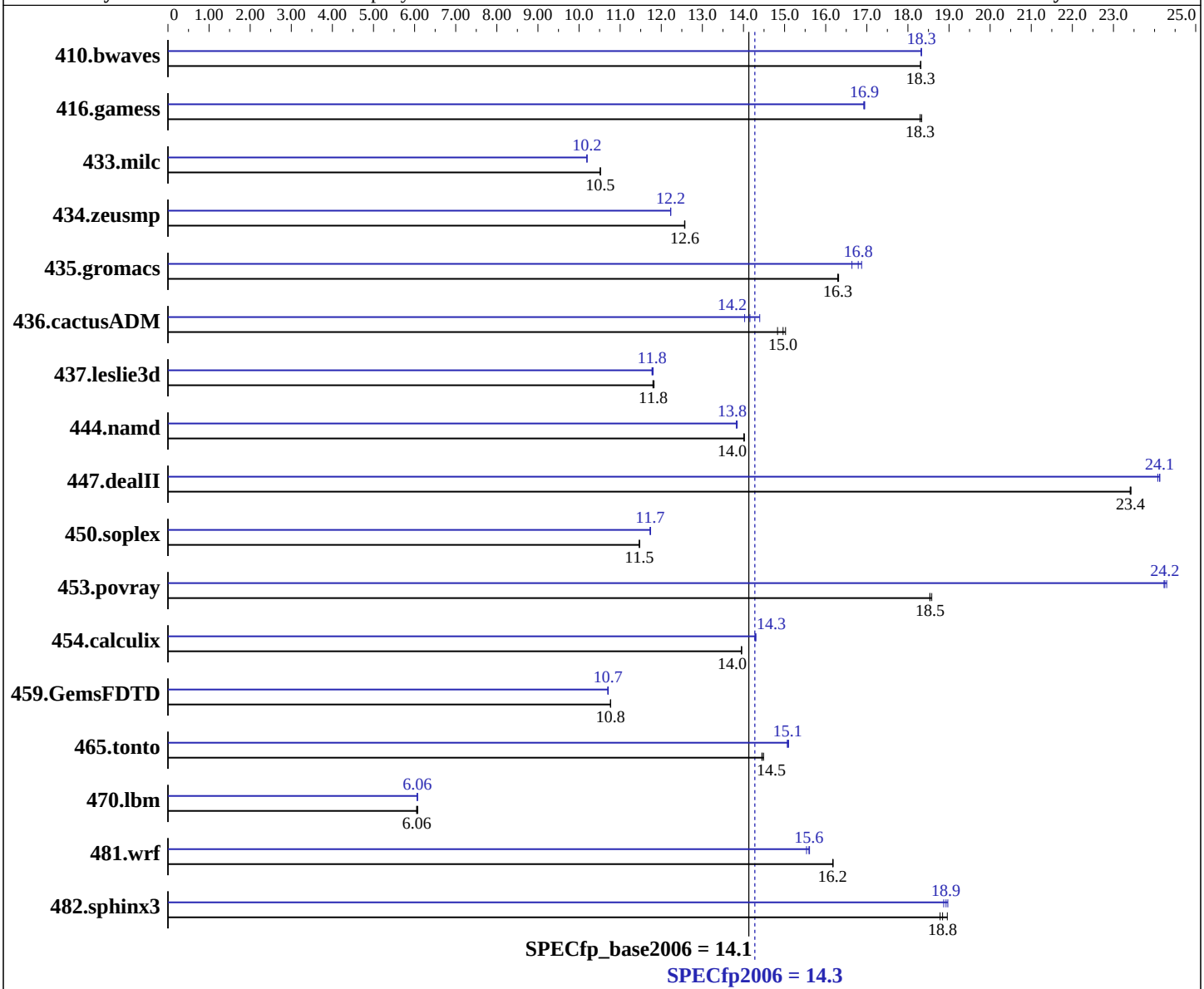
ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®2006 = 14.3

SPECfp_base2006 = 14.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	8 GB (4x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	4x36 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**742**	**18.3**	742	18.3	742	18.3	742	18.3	**741**	**18.3**	741	18.3
416.gamess	1068	18.3	**1070**	**18.3**	1070	18.3	1157	16.9	**1156**	**16.9**	1155	16.9
433.milc	**873**	**10.5**	873	10.5	873	10.5	**900**	**10.2**	901	10.2	900	10.2
434.zeusmp	**724**	**12.6**	724	12.6	724	12.6	744	12.2	**744**	**12.2**	744	12.2
435.gromacs	438	16.3	**438**	**16.3**	438	16.3	423	16.9	429	16.6	**425**	**16.8**
436.cactusADM	795	15.0	806	14.8	**799**	**15.0**	**844**	**14.2**	830	14.4	852	14.0
437.leslie3d	**796**	**11.8**	795	11.8	797	11.8	796	11.8	**798**	**11.8**	798	11.8
444.namd	**572**	**14.0**	572	14.0	572	14.0	580	13.8	**580**	**13.8**	579	13.8
447.dealII	488	23.4	489	23.4	**489**	**23.4**	474	24.1	475	24.1	**474**	**24.1**
450.soplex	**727**	**11.5**	727	11.5	728	11.5	710	11.7	**711**	**11.7**	711	11.7
453.povray	287	18.5	286	18.6	**287**	**18.5**	220	24.2	**219**	**24.2**	219	24.3
454.calculix	591	14.0	**591**	**14.0**	591	13.9	578	14.3	**577**	**14.3**	577	14.3
459.GemsFDTD	985	10.8	986	10.8	**985**	**10.8**	992	10.7	991	10.7	**991**	**10.7**
465.tonto	679	14.5	681	14.4	**681**	**14.5**	652	15.1	**653**	**15.1**	653	15.1
470.lbm	2261	6.08	**2268**	**6.06**	2272	6.05	2262	6.07	**2266**	**6.06**	2268	6.06
481.wrf	690	16.2	**691**	**16.2**	691	16.2	716	15.6	**716**	**15.6**	719	15.5
482.sphinx3	1028	19.0	**1034**	**18.8**	1038	18.8	1027	19.0	1033	18.9	**1030**	**18.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set
Single processor kernel used
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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