Hewlett-Packard Company
ProLiant DL380 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®2006 = 14.5
SPECfp_base2006 = 14.3

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Test date: Jan-2007

Hardware
CPU Name: Intel Xeon X5355
CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software
Operating System: SuSE Linux Enterprise Server 10 EM64T
Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
Package ID 1_cc_c_9.1.045 Build no 20061101
Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
Package ID 1_fc_c_9.1.040 Build no 20061101
Auto Parallel: No

Continued on next page
## SPEC CFP2006 Result

### Hewlett-Packard Company

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| SPECfp2006 | 14.5 |
| SPECfp_base2006 | 14.3 |

### CPU2006 license: 3  
Test sponsor: Hewlett-Packard Company  
Tested by: Hewlett-Packard Company

- L3 Cache: None  
- Other Cache: None  
- Memory: 16 GB (8x2 GB PC2-5300 CL5)  
- Disk Subsystem: 4x36 GB 10 K SAS  
- Other Hardware: None

- File System: ext2  
- System State: Multi-user run level 3  
- Base Pointers: 64-bit  
- Peak Pointers: 32/64-bit  
- Other Software: None

### Results Table

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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</table>

### Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.  
"ulimit -s unlimited" set  
Single processor kernel used

### Base Compiler Invocation

- C benchmarks:  
  ```bash
  icc
  ```

- C++ benchmarks:  
  ```bash
  icpc
  ```

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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**Standard Performance Evaluation Corporation**  
info@spec.org  
http://www.spec.org/
SPEC CFP2006 Result

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Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:
  ifort

Benchmarks using both Fortran and C:
  icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -fast

C++ benchmarks:
  -fast

Fortran benchmarks:
  -fast

Benchmarks using both Fortran and C:
  -fast

Peak Compiler Invocation

C benchmarks:
  icc

Continued on next page
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Peak Compiler Invocation (Continued)

C++ benchmarks:  
icc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:  
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:  
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:  
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:  
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at  
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:  
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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