# SPEC® CFP2006 Result

**Intel Corporation**

Intel DG965WH motherboard  
(2.40 GHz, Intel Core 2 Duo processor E6600)

<table>
<thead>
<tr>
<th>SPECfp®2006 =</th>
<th>Not Run</th>
</tr>
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<tbody>
<tr>
<td>SPECfp_base2006 =</td>
<td>14.5</td>
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**CPU2006 license:** 13  
**Test sponsor:** Intel Corporation  
**Tested by:** Intel Corporation  
**Test date:** Jan-2007  
**Hardware Availability:** Aug-2006  
**Software Availability:** Aug-2006

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### Software

- **Operating System:** Windows XP Professional w/ SP2
- **Compiler:** Intel C++ and Fortran Compiler for IA32 version 9.1 Build no 20060816
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit

### Hardware

- **CPU Name:** Intel Core 2 Duo E6600
- **CPU Characteristics:** 2.40 GHz, 1066 MHz bus
- **CPU MHz:** 2400
- **FPU:** Integrated
- **CPU(s) enabled:** 2 cores, 1 chip, 2 cores/chip
- **CPU(s) orderable:** 1 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 4 MB I+D on chip per chip

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### Results

<table>
<thead>
<tr>
<th>Test Case</th>
<th>SPECfp</th>
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<td>410.bwaves</td>
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L3 Cache: None
Other Cache: None
Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem: Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)
Other Hardware: SoundBlaster Live! PCI card
Peak Pointers: Not Applicable
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 1/2007:
The system bus runs at 1066 MHz
System was configured with Single nVidia Quad SLI Geforce 7950 GX2

Base Compiler Invocation
C benchmarks:
   icl -Qvc7.1 -Qc99

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F9500000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F9500000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F9500000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F9500000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.06.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.06.xml
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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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