Hewlett-Packard Company

ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

SPECint®2006 = 16.1
SPECint_base2006 = 15.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon X5355</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>2.66GHz, 4x2 MB L2 shared, 1333 MHz bus</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2660</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>4 cores, 1 chip, 4 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>8 MB l+D on chip per chip, 4 MB shared / 2 cores</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>16 GB (8x2 GB PC2-5300F CL5)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>1x72 GB 10 K SAS</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>Windows Server 2003 Enterprise X64 Edition</td>
</tr>
<tr>
<td>Compiler</td>
<td>Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>No</td>
</tr>
<tr>
<td>File System</td>
<td>NTFS</td>
</tr>
<tr>
<td>System State</td>
<td>Default</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Other Software</td>
<td>MicroQuill SmartHeap Library 8.0</td>
</tr>
</tbody>
</table>
SPEC CINT2006 Result

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>513</td>
<td>19.0</td>
<td>512</td>
<td>19.1</td>
<td>511</td>
<td>19.1</td>
<td>471</td>
<td>20.7</td>
<td>471</td>
<td>20.7</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>701</td>
<td>13.8</td>
<td>702</td>
<td>13.7</td>
<td>703</td>
<td>13.7</td>
<td>683</td>
<td>14.1</td>
<td>683</td>
<td>14.1</td>
</tr>
<tr>
<td>403.gcc</td>
<td>754</td>
<td>10.7</td>
<td>755</td>
<td>10.7</td>
<td>756</td>
<td>10.7</td>
<td>736</td>
<td>10.9</td>
<td>736</td>
<td>10.9</td>
</tr>
<tr>
<td>429.mcf</td>
<td>515</td>
<td>17.7</td>
<td>515</td>
<td>17.7</td>
<td>515</td>
<td>17.7</td>
<td>515</td>
<td>17.7</td>
<td>515</td>
<td>17.7</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>605</td>
<td>17.3</td>
<td>605</td>
<td>17.3</td>
<td>605</td>
<td>17.3</td>
<td>536</td>
<td>19.6</td>
<td>536</td>
<td>19.6</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>838</td>
<td>11.1</td>
<td>837</td>
<td>11.1</td>
<td>838</td>
<td>11.1</td>
<td>814</td>
<td>11.5</td>
<td>814</td>
<td>11.5</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>479</td>
<td>16.2</td>
<td>749</td>
<td>16.1</td>
<td>749</td>
<td>16.2</td>
<td>689</td>
<td>17.6</td>
<td>689</td>
<td>17.6</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>1376</td>
<td>15.1</td>
<td>1377</td>
<td>15.0</td>
<td>1377</td>
<td>15.0</td>
<td>1364</td>
<td>15.2</td>
<td>1365</td>
<td>15.2</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>796</td>
<td>27.8</td>
<td>796</td>
<td>27.8</td>
<td>797</td>
<td>27.8</td>
<td>780</td>
<td>28.4</td>
<td>779</td>
<td>28.4</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>538</td>
<td>11.6</td>
<td>537</td>
<td>11.6</td>
<td>538</td>
<td>11.6</td>
<td>487</td>
<td>12.8</td>
<td>487</td>
<td>12.8</td>
</tr>
<tr>
<td>473.astar</td>
<td>565</td>
<td>12.4</td>
<td>564</td>
<td>12.4</td>
<td>565</td>
<td>12.4</td>
<td>562</td>
<td>12.5</td>
<td>563</td>
<td>12.5</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>352</td>
<td>19.6</td>
<td>352</td>
<td>19.6</td>
<td>352</td>
<td>19.6</td>
<td>350</td>
<td>19.7</td>
<td>350</td>
<td>19.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"start /b /wait /affinity" used to bind processes to CPU(s).

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc7.1 -Qc99
C++ benchmarks:
icl -Qc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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