Hewlett-Packard Company

ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint®2006 = 16.2

SPECint_base2006 = 15.5

Hardware

CPU Name: Intel Xeon X5355
CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB L3 on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem: 2x72 GB 10k SAS
Other Hardware: None

Software

Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
Package ID: W_CC_P_9.1.020
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0
**SPEC CINT2006 Result**

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---

**CPU2006 license:** 3

**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>509</td>
<td>19.2</td>
<td>509</td>
<td>19.2</td>
<td>509</td>
<td>19.2</td>
<td>465</td>
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<td>401.bzip2</td>
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<td>350</td>
<td>19.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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**Platform Notes**

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

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**Base Compiler Invocation**

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

---

**Base Portability Flags**

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

---

**Base Optimization Flags**

C benchmarks:

```
-fast /F512000000 shlw32m.lib

-link /FORCE:MULTIPLE
```

Continued on next page
SPEC CINT2006 Result

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Test date: Feb-2007
Tested by: Hewlett-Packard Company
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
  shlw32m.lib -link /FORCE:MULTIPLE
  401.bzip2: Same as 400.perlbench
  403.gcc: Same as 400.perlbench
  429.mcf: basepeak = yes
  445.gobmk: Same as 400.perlbench
  456.hmmer: Same as 400.perlbench
  458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca