SPEC® CFP2006 Result

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Hewlett-Packard Company

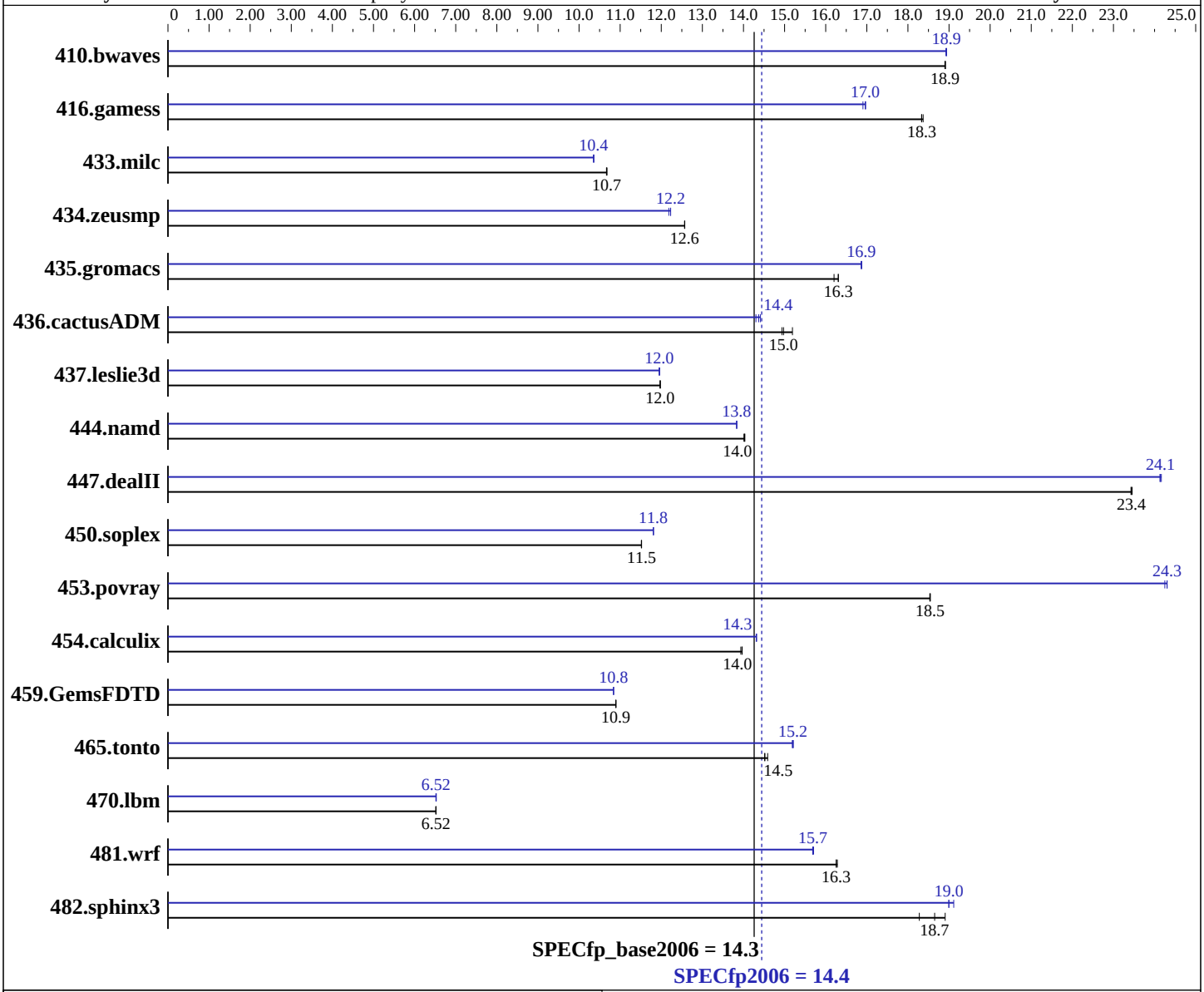
ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®2006 = 14.4

SPECfp_base2006 = 14.3

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB SAS, 10 K RPM	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**719**	**18.9**	719	18.9	719	18.9	718	18.9	**718**	**18.9**	718	18.9
416.gamess	**1068**	**18.3**	1066	18.4	1068	18.3	1154	17.0	1158	16.9	**1154**	**17.0**
433.milc	**860**	**10.7**	861	10.7	859	10.7	887	10.4	886	10.4	**886**	**10.4**
434.zeusmp	**724**	**12.6**	724	12.6	724	12.6	744	12.2	747	12.2	**744**	**12.2**
435.gromacs	**438**	**16.3**	438	16.3	441	16.2	423	16.9	**423**	**16.9**	423	16.9
436.cactusADM	787	15.2	**798**	**15.0**	800	14.9	836	14.3	829	14.4	**832**	**14.4**
437.leslie3d	785	12.0	785	12.0	**785**	**12.0**	**787**	**12.0**	786	12.0	787	11.9
444.namd	572	14.0	571	14.0	**572**	**14.0**	580	13.8	**580**	**13.8**	580	13.8
447.dealII	**488**	**23.4**	488	23.4	488	23.5	474	24.1	**474**	**24.1**	473	24.2
450.soplex	724	11.5	724	11.5	**724**	**11.5**	**706**	**11.8**	706	11.8	706	11.8
453.povray	287	18.5	287	18.5	**287**	**18.5**	**219**	**24.3**	219	24.3	219	24.2
454.calculix	592	13.9	591	14.0	**591**	**14.0**	576	14.3	577	14.3	**576**	**14.3**
459.GemsFDTD	974	10.9	974	10.9	**974**	**10.9**	979	10.8	979	10.8	**979**	**10.8**
465.tonto	678	14.5	675	14.6	**678**	**14.5**	647	15.2	**647**	**15.2**	648	15.2
470.lbm	2107	6.52	2108	6.52	**2108**	**6.52**	2105	6.53	**2107**	**6.52**	2108	6.52
481.wrf	687	16.3	**687**	**16.3**	686	16.3	711	15.7	712	15.7	**712**	**15.7**
482.sphinx3	1031	18.9	1066	18.3	**1045**	**18.7**	1020	19.1	1026	19.0	**1026**	**19.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
Single processor kernel used
```

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:24:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 February 2007.