### Hardware
- **CPU Name:** Intel Xeon X5355  
- **CPU Characteristics:** 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus  
- **CPU MHz:** 2666  
- **FPU:** Integrated  
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 8 MB L1D on chip per chip, 4 MB shared / 2 cores  
- **L3 Cache:** None  
- **Other Cache:** None  
- **Memory:** 16 GB (8x2 GB PC2-5300 CL5)  
- **Disk Subsystem:** 2x72 GB 10k SAS  
- **Other Hardware:** None

### Software
- **Operating System:** Windows Server 2003 Enterprise X64 Edition  
- **Compiler:** Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z  
- **Package ID:** W_CC_P_9.1.020  
- **Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)**  
- **Auto Parallel:** No  
- **File System:** NTFS  
- **System State:** Default  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32-bit  
- **Other Software:** MicroQuill SmartHeap Library 8.0

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**SPEC CINT2006 Result**

**Hewlett-Packard Company**

**ProLiant BL20p G4**  
(2.66 GHz, Intel Xeon processor X5355)

**SPEClnt®2006 = 16.4**
**SPEClnt_base2006 = 15.7**

- **CPU2006 license:** 3  
- **Test date:** Feb-2007  
- **Test sponsor:** Hewlett-Packard Company  
- **Hardware Availability:** Jan-2007  
- **Tested by:** Hewlett-Packard Company  
- **Software Availability:** Nov-2006  
- **Test date:** Feb-2007  
- **Hardware Availability:** Jan-2007  
- **Tested by:** Hewlett-Packard Company  
- **Software Availability:** Nov-2006

### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbanch</td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>14.2</td>
</tr>
<tr>
<td>403.gcc</td>
<td>11.2</td>
</tr>
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<tr>
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</tr>
<tr>
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<td>17.5</td>
</tr>
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<tr>
<td>462.libquantum</td>
<td>15.4</td>
</tr>
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</tr>
<tr>
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<td>13.4</td>
</tr>
<tr>
<td>473.astar</td>
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</tr>
<tr>
<td>483.xalancbmk</td>
<td>12.7</td>
</tr>
</tbody>
</table>

**SPECint_base2006 = 15.7**
**SPEC CINT2006 Result**

**Hewlett-Packard Company**

ProLiant BL20p G4 (2.66 GHz, Intel Xeon processor X5355)

**SPECint2006 =** 16.4

**SPECint_base2006 =** 15.7

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**CPU2006 license:** 3

**Test date:** Feb-2007

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**Hardware Availability:** Jan-2007

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## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Base</strong></td>
<td><strong>Peak</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>507</td>
<td>19.3</td>
<td>507</td>
<td>19.3</td>
<td>463</td>
<td>21.1</td>
<td>463</td>
<td>21.1</td>
<td>463</td>
<td>21.1</td>
</tr>
<tr>
<td>403.gcc</td>
<td>737</td>
<td>10.9</td>
<td>737</td>
<td>10.9</td>
<td>719</td>
<td>11.2</td>
<td>718</td>
<td>11.2</td>
<td>719</td>
<td>11.2</td>
</tr>
<tr>
<td>429.mcf</td>
<td>492</td>
<td>18.5</td>
<td>492</td>
<td>18.5</td>
<td>492</td>
<td>18.5</td>
<td>492</td>
<td>18.5</td>
<td>492</td>
<td>18.5</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>601</td>
<td>17.5</td>
<td>601</td>
<td>17.5</td>
<td>532</td>
<td>19.7</td>
<td>532</td>
<td>19.7</td>
<td>532</td>
<td>19.7</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>835</td>
<td>11.2</td>
<td>836</td>
<td>11.2</td>
<td>835</td>
<td>11.2</td>
<td>812</td>
<td>11.5</td>
<td>813</td>
<td>11.5</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>747</td>
<td>16.2</td>
<td>748</td>
<td>16.2</td>
<td>748</td>
<td>16.2</td>
<td>688</td>
<td>17.6</td>
<td>688</td>
<td>17.6</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>1366</td>
<td>15.2</td>
<td>1366</td>
<td>15.2</td>
<td>1350</td>
<td>15.4</td>
<td>1350</td>
<td>15.4</td>
<td>1350</td>
<td>15.4</td>
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<td>792</td>
<td>27.9</td>
<td>792</td>
<td>28.0</td>
<td>792</td>
<td>28.0</td>
<td>776</td>
<td>28.5</td>
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<td>19.7</td>
<td>351</td>
<td>19.6</td>
<td>351</td>
<td>19.7</td>
<td>349</td>
<td>19.8</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS. Adjacent Sector Prefetch disabled in BIOS.

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### Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

---

### Base Portability Flags

- **403.gcc:** -DSPEC_CPU_WIN32
- **464.h264ref:** -DSPEC_CPU_NO_INNTYPES -DWIN32

---

### Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib
```

```
-link /FORCE:MULTIPLE
```

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Continued on next page
SPEC CINT2006 Result

Hewlett-Packard Company
ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

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SPECint_base2006 = 15.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES "DWIN32"

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
  shlw32m.lib -link /FORCE:MULTIPLE
  401.bzip2: Same as 400.perlbench
  403.gcc: Same as 400.perlbench
  429.mcf: basepeak = yes
  445.gobmk: Same as 400.perlbench
  456.hmmer: Same as 400.perlbench
  458.sjeng: Same as 400.perlbench

Continued on next page
Hewlett-Packard Company

ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

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### Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

- `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features`  
- `/F512000000 shlw32m.lib`  
- `-link /FORCE:MULTIPLE`

### Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at:

http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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