### SPECint® CINT2006 Result

**Hewlett-Packard Company**

ProLiant BL20p G4  
(2.66 GHz, Intel Xeon processor X5355)

**SPECint_rate2006 = 82.2**  
**SPECint_rate_base2006 = 79.6**

<table>
<thead>
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<th>Test date:</th>
<th>Feb-2007</th>
<th>Hardware Availability: Jan-2007</th>
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<td>Test sponsor:</td>
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<tr>
<td>CPU2006 license:</td>
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**Hardware**

- **CPU Name:** Intel Xeon X5355  
- **CPU Characteristics:** 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus  
- **CPU MHZ:** 2666  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
- **L3 Cache:** None  
- **Other Cache:** None  
- **Memory:** 16 GB (8x2 GB PC2-5300 CL5)  
- **Disk Subsystem:** 2x72 GB 10k SAS  
- **Other Hardware:** None

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**Software**

- **Operating System:** Windows Server 2003 Enterprise X64 Edition  
- **Compiler:** Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z  
- **Package ID:** W_CC_P_9.1.020  
- **Microsoft Visual Studio .NET 2003** (v7.1.3088, for libraries)  
- **Auto Parallel:** No  
- **File System:** NTFS  
- **System State:** Default  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32-bit  
- **Other Software:** MicroQuill SmartHeap Library 8.0

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**Copies**

| Program | 10.0 | 20.0 | 30.0 | 40.0 | 50.0 | 60.0 | 70.0 | 80.0 | 90.0 | 100 | 110 | 120 | 130 | 140 | 150 | 160 | 170 | 180 | 190 | 200 | 210 | 220 |
|---------|------|------|------|------|------|------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
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| 458.sjeng | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| 462.libquantum | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| 464.h264ref | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| 471.omnetpp | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| 473.astar | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| 483.xalancbmk | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 | 8 |

**SPECint_rate_base2006 = 79.6**

**SPECint_rate2006 = 82.2**
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

## Base Compiler Invocation

C benchmarks:
- icl -Qvc7.1 -Qc99

C++ benchmarks:
- icl -Qvc7.1

## Base Portability Flags

- 403.gcc: -DSPEC_CPU_WIN32
- 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:
- -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
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Tested by: Hewlett-Packard Company
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Base Optimization Flags (Continued)

C++ benchmarks:
   -fast -Qcxx_features /F512000000 shlw32m.lib
   -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
   403.gcc: -Daloca=_alloca

Peak Compiler Invocation

C benchmarks:
   icl -Qvc7.1 -Qc99

C++ benchmarks:
   icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
   400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
   shlw32m.lib
   -link /FORCE:MULTIPLE

   401.bzip2: Same as 400.perlbench
   403.gcc: Same as 400.perlbench
   429.mcf: basepeak = yes
   445.gobmk: Same as 400.perlbench
   456.hmmer: Same as 400.perlbench
   458.sjeng: Same as 400.perlbench

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
- -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 шlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006(flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006(flags/hp-ic91-flags.20090715.02.xml

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Tested with SPEC CPU2006 v1.0.
Originally published on 20 February 2007.