



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = **Not Run**

IBM BladeCenter HS21 XM (Intel Xeon 5110)

SPECint_base2006 = **10.0**

CPU2006 license: 11

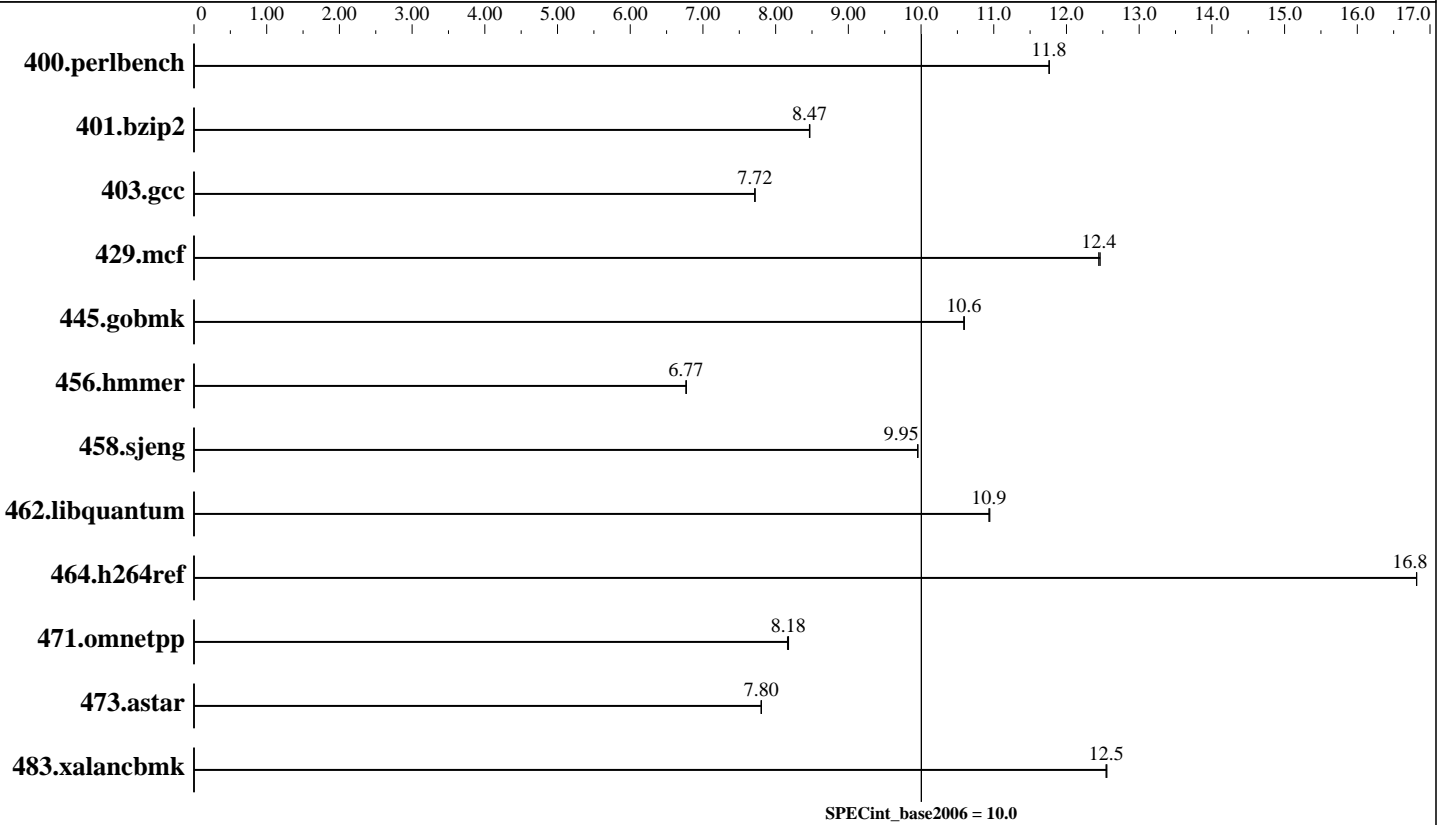
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 5110
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816
 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	831	11.8	831	11.8	<u>831</u>	<u>11.8</u>						
401.bzip2	<u>1140</u>	<u>8.47</u>	1140	8.46	1139	8.47						
403.gcc	1043	7.72	1044	7.71	<u>1043</u>	<u>7.72</u>						
429.mcf	<u>733</u>	<u>12.4</u>	733	12.4	732	12.5						
445.gobmk	990	10.6	991	10.6	<u>991</u>	<u>10.6</u>						
456.hammer	1379	6.77	1378	6.77	<u>1378</u>	<u>6.77</u>						
458.sjeng	<u>1216</u>	<u>9.95</u>	1216	9.95	1216	9.95						
462.libquantum	1895	10.9	<u>1895</u>	<u>10.9</u>	1893	10.9						
464.h264ref	1316	16.8	<u>1316</u>	<u>16.8</u>	1316	16.8						
471.omnetpp	766	8.16	<u>764</u>	<u>8.18</u>	764	8.18						
473.astar	899	7.81	900	7.80	<u>900</u>	<u>7.80</u>						
483.xalancbmk	550	12.6	550	12.5	<u>550</u>	<u>12.5</u>						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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