SPEC® CINT2006 Result

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IBM Corporation

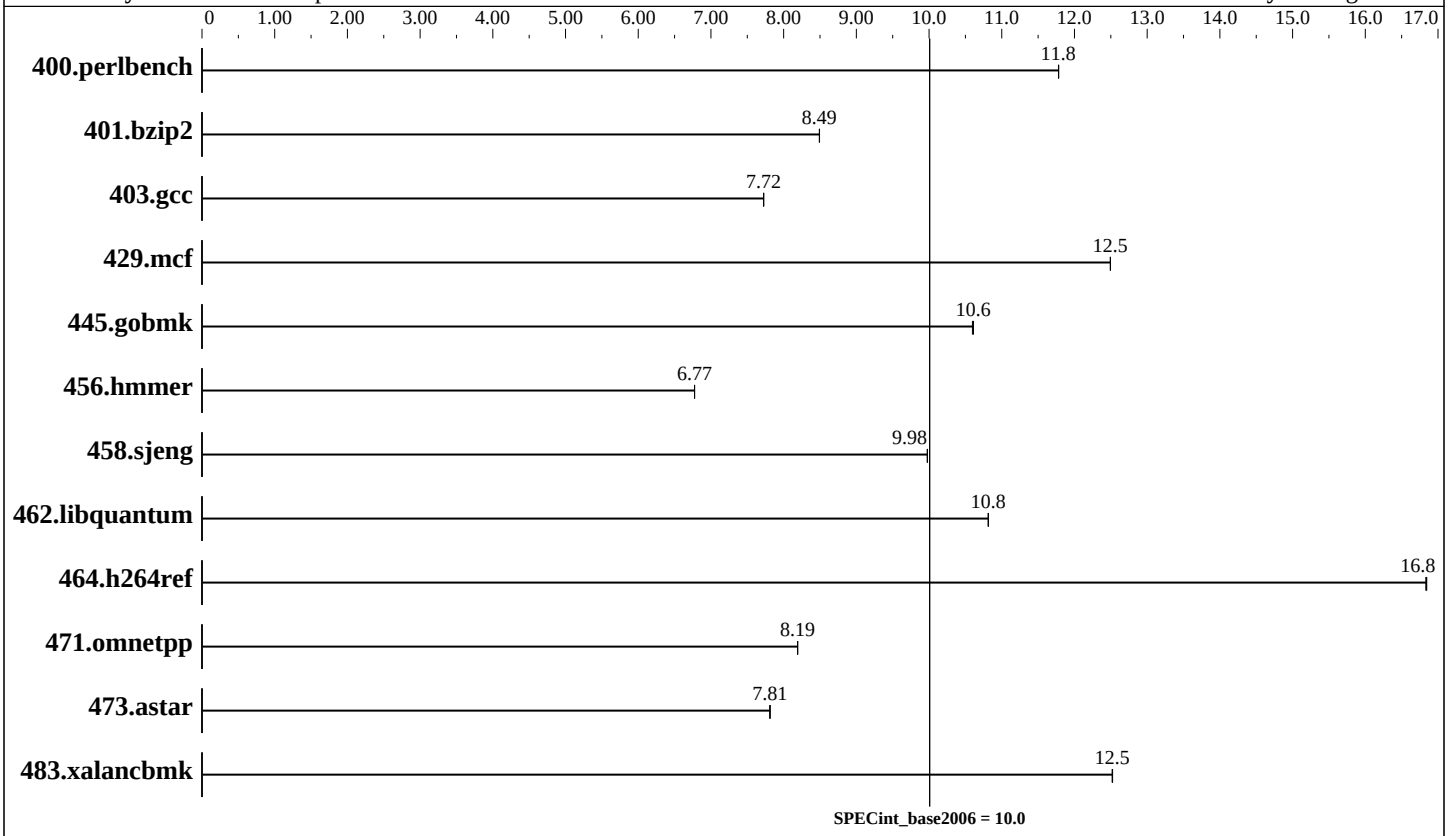
IBM BladeCenter HS21 XM (Intel Xeon E5310)

SPECint®2006 = Not Run

SPECint_base2006 = 10.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	829	11.8	829	11.8	**829**	**11.8**						
401.bzip2	1136	8.49	**1136**	**8.49**	1136	8.49						
403.gcc	1042	7.73	1042	7.72	**1042**	**7.72**						
429.mcf	730	12.5	730	12.5	**730**	**12.5**						
445.gobmk	**989**	**10.6**	989	10.6	990	10.6						
456.hmmer	1377	6.77	**1377**	**6.77**	1377	6.77						
458.sjeng	1213	9.98	**1213**	**9.98**	1213	9.98						
462.libquantum	1916	10.8	**1916**	**10.8**	1916	10.8						
464.h264ref	1315	16.8	**1314**	**16.8**	1314	16.8						
471.omnetpp	763	8.19	**763**	**8.19**	763	8.19						
473.astar	898	7.82	**899**	**7.81**	899	7.81						
483.xalancbmk	551	12.5	551	12.5	**551**	**12.5**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:35:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 March 2007.