SPEC® CINT2006 Result

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IBM Corporation

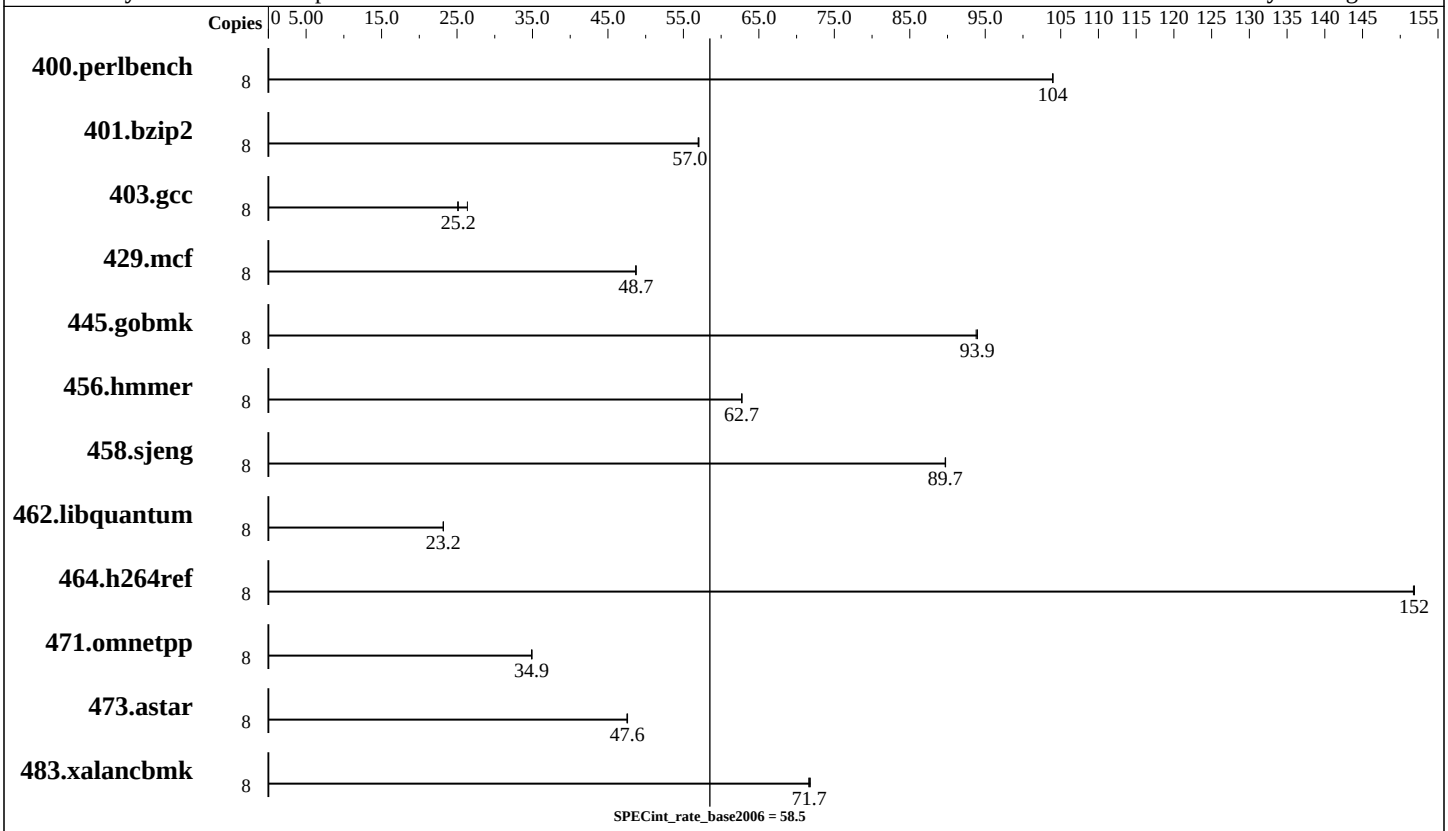
IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 58.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	752	104	752	104	**752**	**104**							
401.bzip2	8	1355	57.0	1353	57.1	**1355**	**57.0**							
403.gcc	8	**2558**	**25.2**	2442	26.4	2567	25.1							
429.mcf	8	1497	48.7	1499	48.7	**1498**	**48.7**							
445.gobmk	8	**893**	**93.9**	895	93.8	893	94.0							
456.hmmer	8	1190	62.7	1190	62.7	**1190**	**62.7**							
458.sjeng	8	**1079**	**89.7**	1079	89.7	1079	89.7							
462.libquantum	8	7150	23.2	**7150**	**23.2**	7149	23.2							
464.h264ref	8	1167	152	1166	152	**1166**	**152**							
471.omnetpp	8	1433	34.9	1431	34.9	**1433**	**34.9**							
473.astar	8	1182	47.5	**1181**	**47.6**	1180	47.6							
483.xalancbmk	8	769	71.8	771	71.6	**770**	**71.7**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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