SPEC® CFP2006 Result

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IBM Corporation

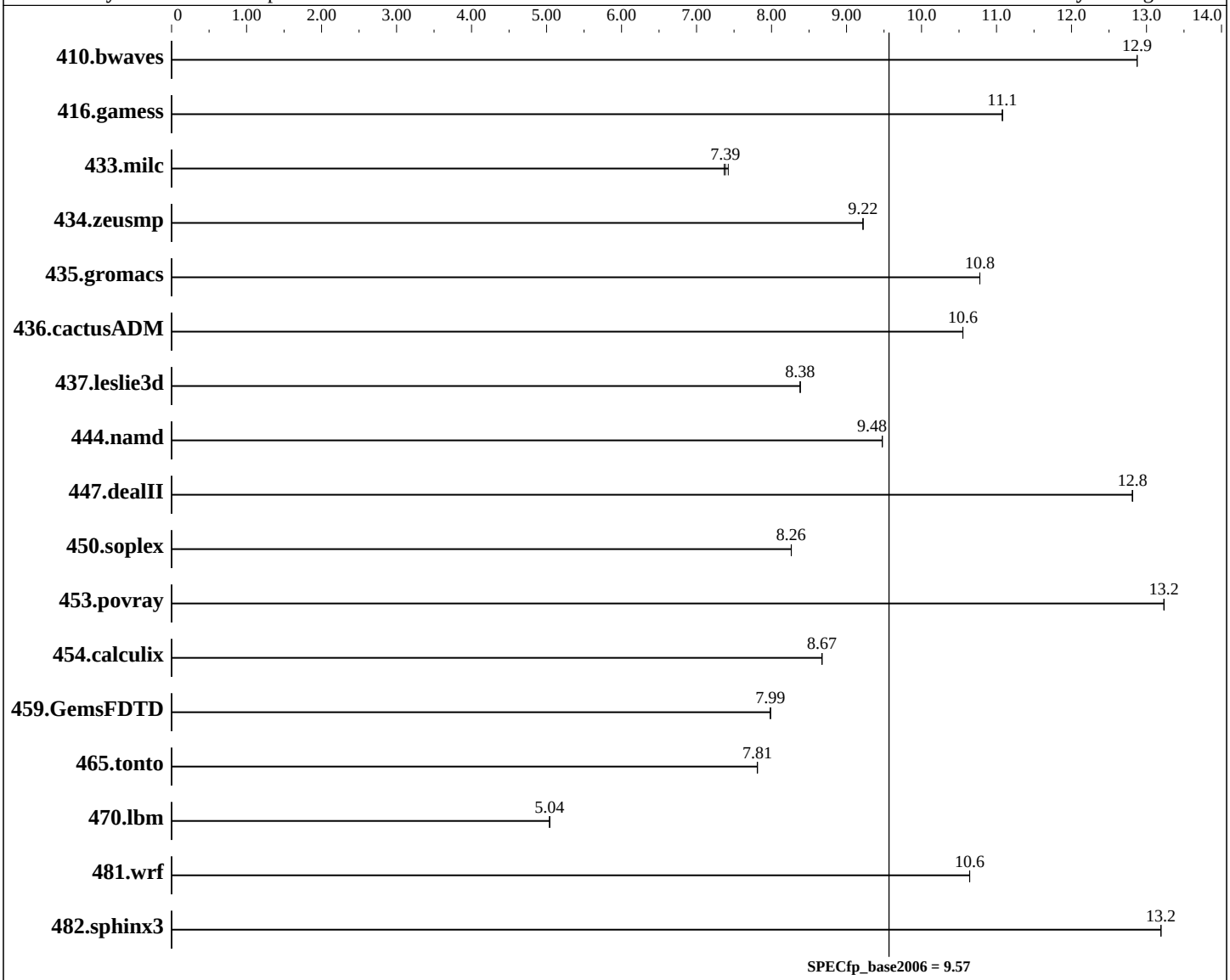
IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECfp®2006 = Not Run

SPECfp_base2006 = 9.57

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32-bit
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)	Peak Pointers:	Not Applicable
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM	Other Software:	Smart Heap Library, Version 8
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	1056	12.9	**1056**	**12.9**	1056	12.9						
416.gamess	1767	11.1	**1767**	**11.1**	1767	11.1						
433.milc	1237	7.42	**1243**	**7.39**	1246	7.37						
434.zeusmp	**987**	**9.22**	987	9.22	987	9.22						
435.gromacs	**662**	**10.8**	662	10.8	663	10.8						
436.cactusADM	1133	10.6	1133	10.6	**1133**	**10.6**						
437.leslie3d	1121	8.38	**1121**	**8.38**	1121	8.38						
444.namd	846	9.48	846	9.48	**846**	**9.48**						
447.dealII	893	12.8	893	12.8	**893**	**12.8**						
450.soplex	1009	8.26	**1009**	**8.26**	1009	8.26						
453.povray	402	13.2	**402**	**13.2**	402	13.2						
454.calculix	951	8.67	951	8.67	**951**	**8.67**						
459.GemsFDTD	**1329**	**7.99**	1329	7.99	1329	7.99						
465.tonto	1260	7.81	**1260**	**7.81**	1259	7.81						
470.lbm	**2726**	**5.04**	2726	5.04	2726	5.04						
481.wrf	1050	10.6	1050	10.6	**1050**	**10.6**						
482.sphinx3	1477	13.2	1477	13.2	**1477**	**13.2**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

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