SPEC® CINT2006 Result

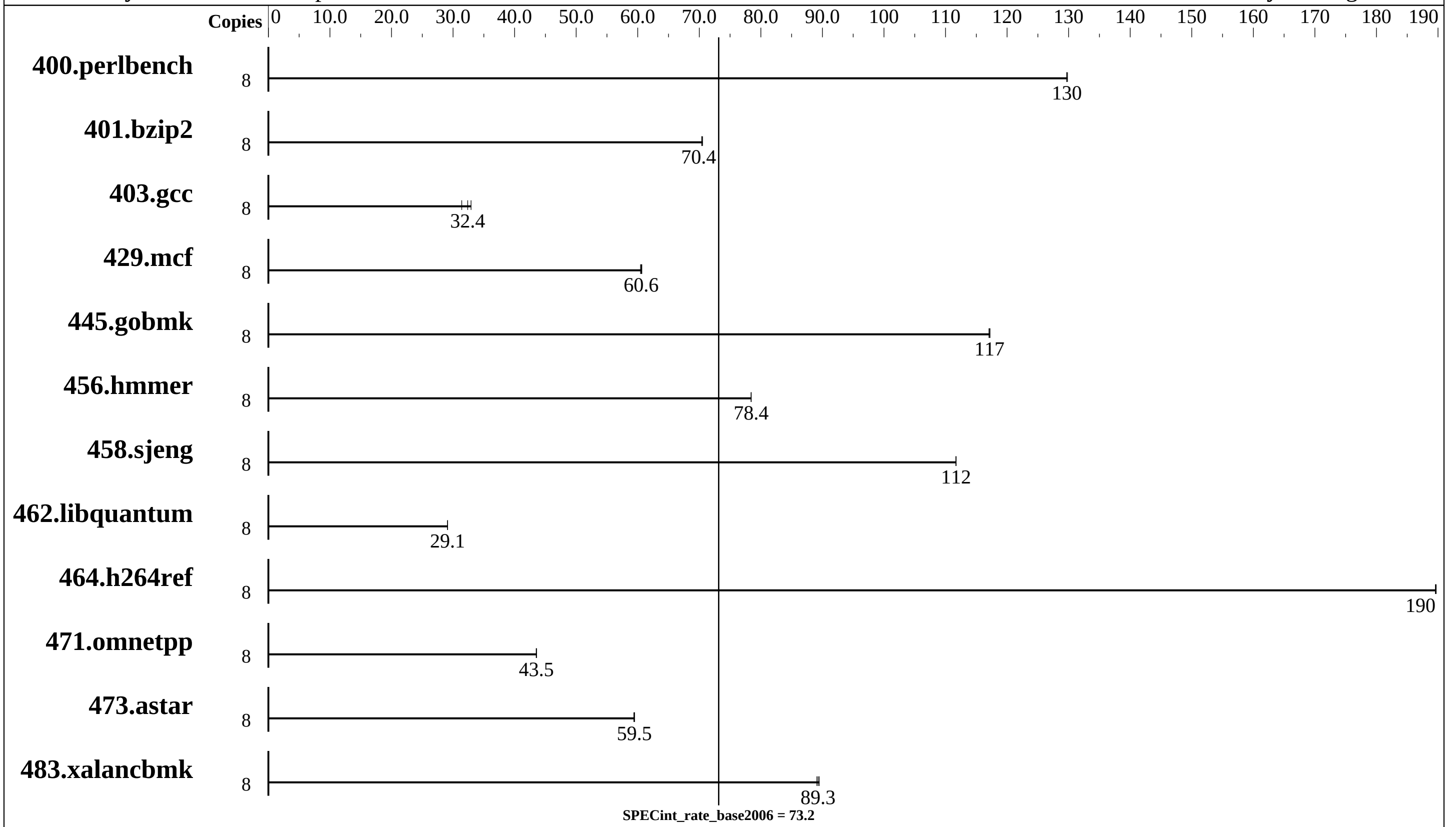
IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon E5345)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 73.2

CPU2006 license:	11	**Test date:**	Jan-2007
Test sponsor:	IBM Corporation	**Hardware Availability:**	Feb-2007
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	8	602	130	603	130	**603**	**130**							
401.bzip2	8	1095	70.5	**1096**	**70.4**	1097	70.4							
403.gcc	8	**1989**	**32.4**	2050	31.4	1956	32.9							
429.mcf	8	1203	60.7	**1205**	**60.6**	1207	60.5							
445.gobmk	8	**716**	**117**	716	117	717	117							
456.hmmer	8	952	78.4	**952**	**78.4**	952	78.4							
458.sjeng	8	867	112	867	112	**867**	**112**							
462.libquantum	8	5693	29.1	**5692**	**29.1**	5692	29.1							
464.h264ref	8	**934**	**190**	933	190	934	190							
471.omnetpp	8	1148	43.6	1149	43.5	**1148**	**43.5**							
473.astar	8	**944**	**59.5**	944	59.5	946	59.4							
483.xalancbmk	8	617	89.5	**618**	**89.3**	620	89.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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