SPEC® CINT2006 Result

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IBM Corporation

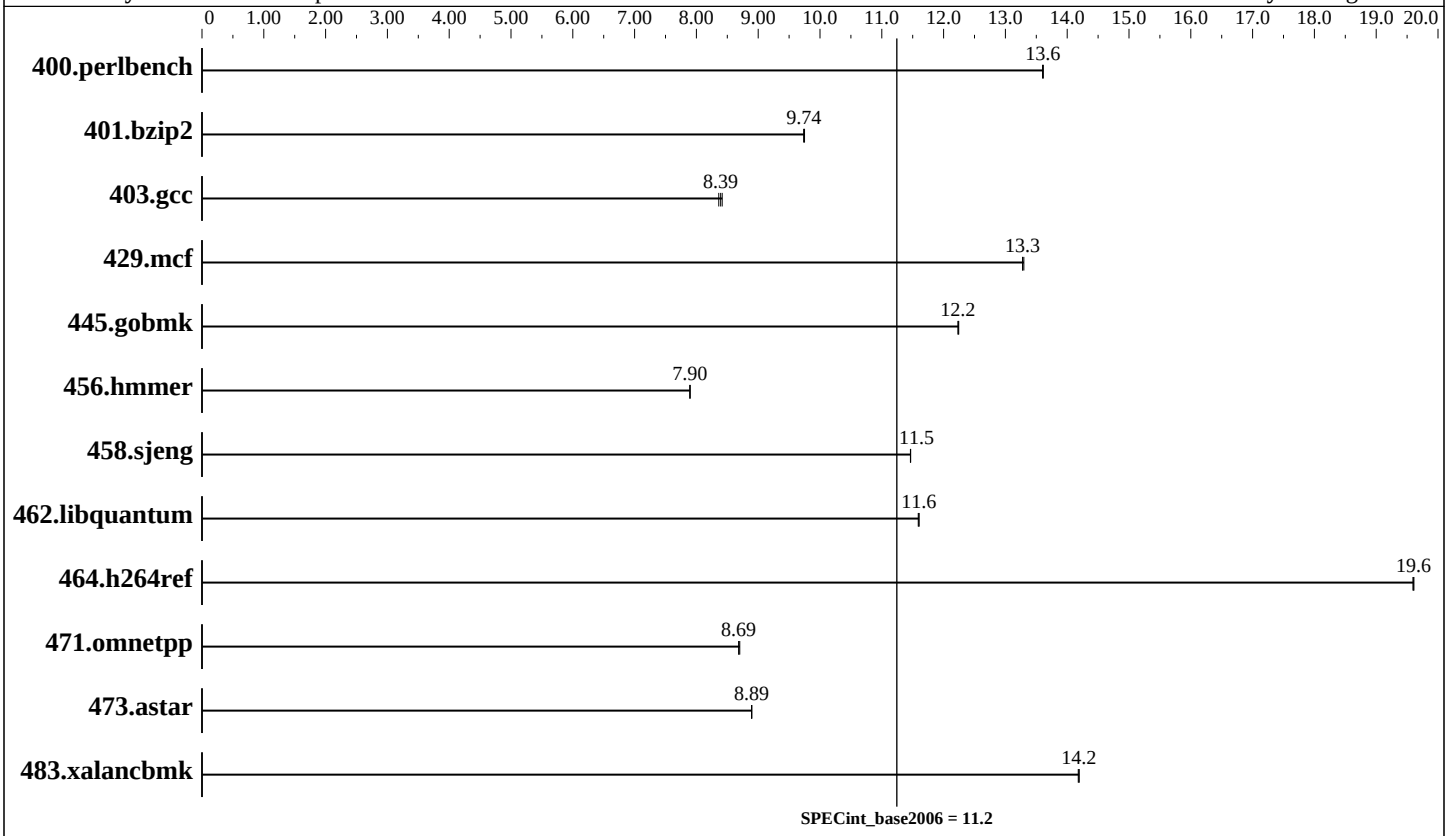
IBM BladeCenter HS21 XM (Intel Xeon 5120)

SPECint®2006 = Not Run

SPECint_base2006 = 11.2

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 5120
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	718	13.6	**718**	**13.6**	718	13.6						
401.bzip2	990	9.75	991	9.74	**991**	**9.74**						
403.gcc	963	8.36	**959**	**8.39**	956	8.42						
429.mcf	687	13.3	686	13.3	**687**	**13.3**						
445.gobmk	**857**	**12.2**	857	12.2	857	12.2						
456.hmmer	**1182**	**7.90**	1182	7.90	1181	7.90						
458.sjeng	1055	11.5	**1055**	**11.5**	1056	11.5						
462.libquantum	1786	11.6	1788	11.6	**1786**	**11.6**						
464.h264ref	1129	19.6	**1129**	**19.6**	1129	19.6						
471.omnetpp	718	8.70	**720**	**8.69**	720	8.69						
473.astar	**789**	**8.89**	789	8.89	789	8.90						
483.xalancbmk	**486**	**14.2**	486	14.2	487	14.2						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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