SPEC® CFP2006 Result

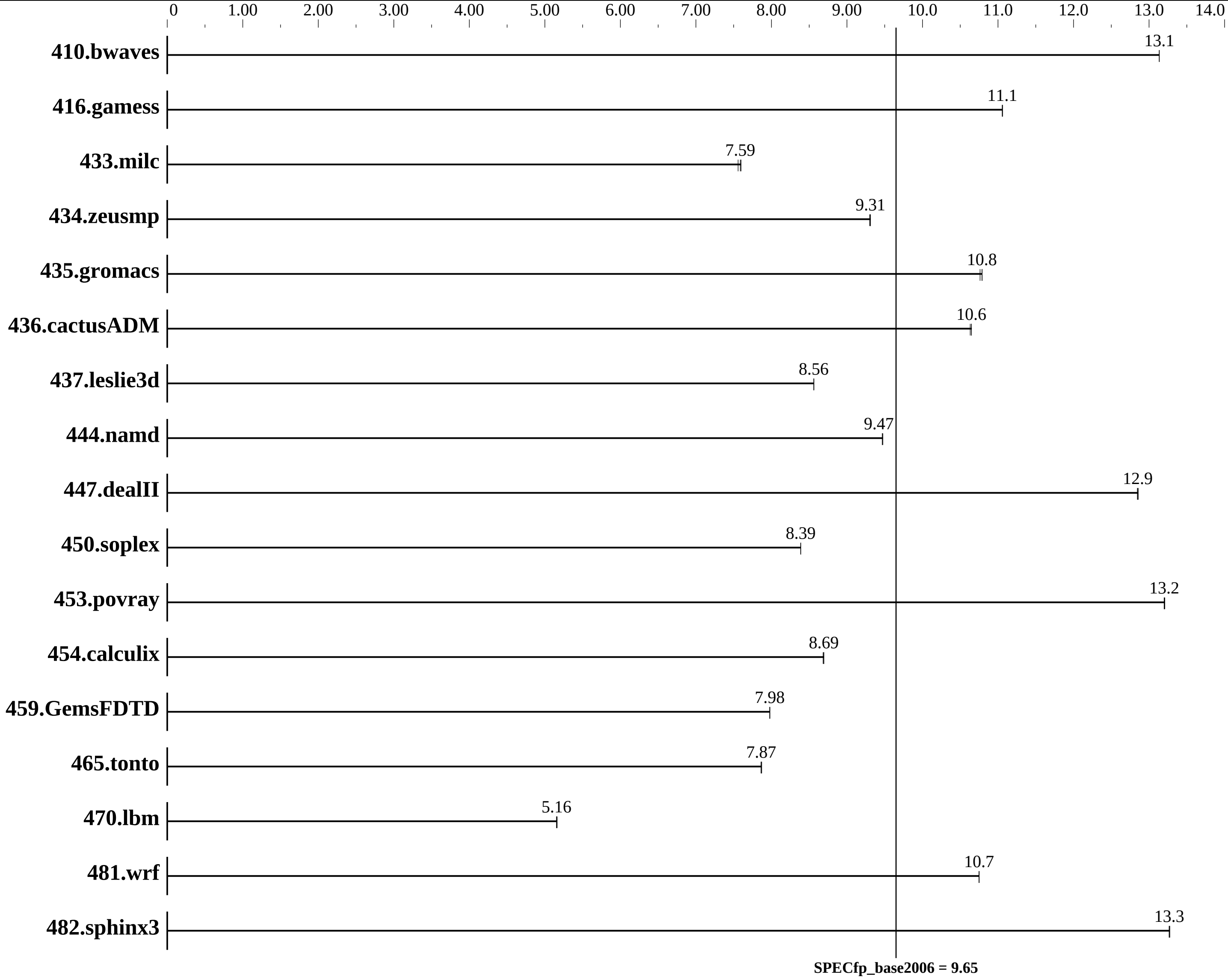
IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon 5120)

SPECfp®2006 = Not Run

SPECfp_base2006 = 9.65

CPU2006 license:	11	**Test date:**	Jan-2007
Test sponsor:	IBM Corporation	**Hardware Availability:**	Feb-2007
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon 5120
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Smart Heap Library, Version 8

Results Table

	Base						Peak					
Benchmark	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	1035	13.1	**1035**	**13.1**	1035	13.1						
416.gamess	1770	11.1	1771	11.1	**1771**	**11.1**						
433.milc	1208	7.60	1215	7.56	**1210**	**7.59**						
434.zeusmp	977	9.31	**977**	**9.31**	979	9.30						
435.gromacs	662	10.8	**662**	**10.8**	663	10.8						
436.cactusADM	**1122**	**10.6**	1122	10.6	1124	10.6						
437.leslie3d	**1098**	**8.56**	1098	8.56	1098	8.56						
444.namd	846	9.48	847	9.47	**847**	**9.47**						
447.dealII	891	12.8	890	12.9	**890**	**12.9**						
450.soplex	994	8.39	**994**	**8.39**	994	8.39						
453.povray	**403**	**13.2**	403	13.2	403	13.2						
454.calculix	949	8.69	950	8.68	**949**	**8.69**						
459.GemsFDTD	1330	7.98	**1330**	**7.98**	1330	7.98						
465.tonto	1250	7.87	1252	7.86	**1251**	**7.87**						
470.lbm	**2665**	**5.16**	2661	5.16	2665	5.16						
481.wrf	1039	10.8	1039	10.7	**1039**	**10.7**						
482.sphinx3	**1469**	**13.3**	1469	13.3	1468	13.3						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

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Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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