## SPECint® Rate 2006 Result

**Itautec**

Servidor Itautec MX201 (Intel Xeon E5335)

<table>
<thead>
<tr>
<th>SPECint®_rate2006</th>
<th>SPECint_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>65.9</td>
<td>65.9</td>
</tr>
</tbody>
</table>

**CPU 2006 license:** 9001  
**Test date:** Feb-2007  
**Test sponsor:** Itautec  
**Hardware Availability:** Feb-2007  
**Tested by:** Itautec  
**Software Availability:** May-2006

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5335</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>1333MHz system bus</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2000</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>8 cores, 2 chips, 4 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1-2 chips</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>8 MB I+D on chip per chip, 4 MB shared / 2 cores</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>16 GB (8x2GB DDR2-RAM PC2-5300F/CAS 5-5-5)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>73 GB SCSI, 10000RPM</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>Intel C++ Compiler for IA32 version 9.1 (for libraries)</td>
</tr>
<tr>
<td>File System</td>
<td>NTFS</td>
</tr>
<tr>
<td>System State</td>
<td>Default</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Other Software</td>
<td>Microquill SmartHeap Library v.8.0 for SMP</td>
</tr>
</tbody>
</table>

---

Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
## General Notes

This result was measured on the Servidor Itautec MX221. The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

## Base Compiler Invocation

### C benchmarks:

```
icl -Qvc7.1 -Qc99
```

### C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`

464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

## Base Optimization Flags

```
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE
```
**SPEC CINT2006 Result**

**Itautec**

**Servidor Itautec MX201 (Intel Xeon E5335)**

| SPECint_rate2006 = | 65.9 |
| SPECint_rate_base2006 = | 65.9 |

- **CPU2006 license:** 9001
- **Test sponsor:** Itautec
- **Tested by:** Itautec
- **Test date:** Feb-2007
- **Hardware Availability:** Feb-2007
- **Software Availability:** May-2006

**Base Optimization Flags (Continued)**

C++ benchmarks:
- -fast -Qcxx_features /F5120000000 shlSMPMt.lib
- -link /FORCE:MULTIPLE

**Base Other Flags**

C benchmarks:
- 403.gcc: -Dalloca=_alloca

**Peak Optimization Flags**

C benchmarks:
- 400.perlbench: basepeak = yes
- 401.bzip2: basepeak = yes
- 403.gcc: basepeak = yes
- 429.mcf: basepeak = yes
- 445.gobmk: basepeak = yes
- 456.hmmer: basepeak = yes
- 458.sjeng: basepeak = yes
- 462.libquantum: basepeak = yes
- 464.h264ref: basepeak = yes

C++ benchmarks:
- 471.omnetpp: basepeak = yes
- 473.astar: basepeak = yes
- 483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml
<table>
<thead>
<tr>
<th>Itautec Servidor Itautec MX201 (Intel Xeon E5335)</th>
<th>SPECint_rate2006 = 65.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006 = 65.9</td>
<td></td>
</tr>
<tr>
<td>CPU2006 license: 9001</td>
<td>Test date: Feb-2007</td>
</tr>
<tr>
<td>Test sponsor: Itautec</td>
<td>Hardware Availability: Feb-2007</td>
</tr>
<tr>
<td>Tested by: Itautec</td>
<td>Software Availability: May-2006</td>
</tr>
</tbody>
</table>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Originally published on 7 March 2007.