Bull SAS

NovaScale T840 (3.00 GHz, Intel Xeon 5160)

**SPECfp®_rate2006 = 40.7**

**SPECfp_rate_base2006 = 40.7**

### Hardware

- **CPU Name:** Intel Xeon 5160
- **CPU Characteristics:** 3.0GHz, 1332MHz bus
- **CPU MHz:** 3000
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 2 chips, 2 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 4 MB I+D on chip per core

### Software

- **Operating System:** Windows Server 2003 Enterprise X64 Edition
- **Compiler:**
  - Intel C++ Compiler 9.1.033 for 32-bit app.
  - Intel Fortran Compiler 9.1.033 for 32-bit app.
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default

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Bull SAS

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SPEC CFP2006 Result

CPU2006 license: 3
Test sponsor: Bull SAS
Tested by: Bull SAS

L3 Cache: None
Other Cache: None
Memory: 8 GB (667 MHz ECC CL5 DDR2 FB-DIMM)
Disk Subsystem: 3x73GB SCSI 15000 rpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
Bull SAS
NovaScale T840 (3.00 GHz, Intel Xeon 5160)

SPEC CFP2006 Result
SPECfp_rate2006 = 40.7
SPECfp_rate_base2006 = 40.7

CPU2006 license: 3
Test sponsor: Bull SAS
Test date: Feb-2007
Tested by: Bull SAS
Hardware Availability: Jul-2006
Software Availability: Dec-2006

Base Compiler Invocation

C benchmarks:
  icl -Qvc7.1 -Qc99

C++ benchmarks:
  icl -Qvc7.1

Fortran benchmarks:
  ifort

Benchmarks using both Fortran and C:
  icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
  444.namd:
  447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
    -DBOOST_NO_INTRINSIC_WCHAR_T
  453.povray: -DSPEC_CPU_WINDOWS_ICL
  454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
    481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
  -fast /F9500000000 shlw32m.lib
  -link /FORCE:MULTIPLE

C++ benchmarks:
  -fast -Qcxx_features /F9500000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Fortran benchmarks:
  -fast /F9500000000
  -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
  -fast /F9500000000
  -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:
  433.milc: basepeak = yes

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<table>
<thead>
<tr>
<th>CPU2006 license:</th>
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**SPEC CFP2006 Result**

**SPECfp_rate2006 = 40.7**

**SPECfp_rate_base2006 = 40.7**

Peak Optimization Flags (Continued)

- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

C++ benchmarks:
- 444.namd: basepeak = yes
- 447.dealII: basepeak = yes
- 450.soplex: basepeak = yes
- 453.povray: basepeak = yes

Fortran benchmarks:
- 410.bwaves: basepeak = yes
- 416.gamess: basepeak = yes
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: basepeak = yes
- 465.tonto: basepeak = yes

Benchmarks using both Fortran and C:
- 435.gromacs: basepeak = yes
- 436.cactusADM: basepeak = yes
- 454.calculix: basepeak = yes
- 481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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