# SPEC® CINT2006 Result

Fujitsu Siemens Computers

CELSIUS R640, Intel Xeon X5355 processor

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>82.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>79.9</td>
</tr>
</tbody>
</table>

## Hardware

- **CPU Name:** Intel Xeon X5355
- **CPU Characteristics:** Dual Core, 2.66 GHz
- **CPU MHz:** 2667
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 16 GB (8x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
- **Disk Subsystem:** SATA II 7200 rpm
- **Other Hardware:** None

## Software

- **Operating System:** Windows XP, 64 bit Edition
- **Compiler:** Intel C++ Compiler for 32-bit applications, - version 9.1, Build 20070109Z
- **Microsoft Visual Studio .NET 2003 (for libraries)**
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** MicroQuill SmartHeap Library 8.0

## Test Details

- **CPU2006 license:** 22
- **Test sponsor:** Fujitsu Siemens Computers
- **Tested by:** Fujitsu Siemens Computers
- **Test date:** Feb-2007
- **Hardware Availability:** Nov-2006
- **Software Availability:** Jan-2007
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Results Table

<table>
<thead>
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<th>Ratio</th>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes
BIOS default settings have been used, except:
High Bandwidth Option Enabled

General Notes
'start /b /wait /affinity' command is used to bind CPU(s) to processes
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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
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Base Optimization Flags

C benchmarks:
- fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
   shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
- -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
- -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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