



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 16.7

CELSIUS R540, Intel Xeon processor X5355

SPECint_base2006 = 16.0

CPU2006 license: 22

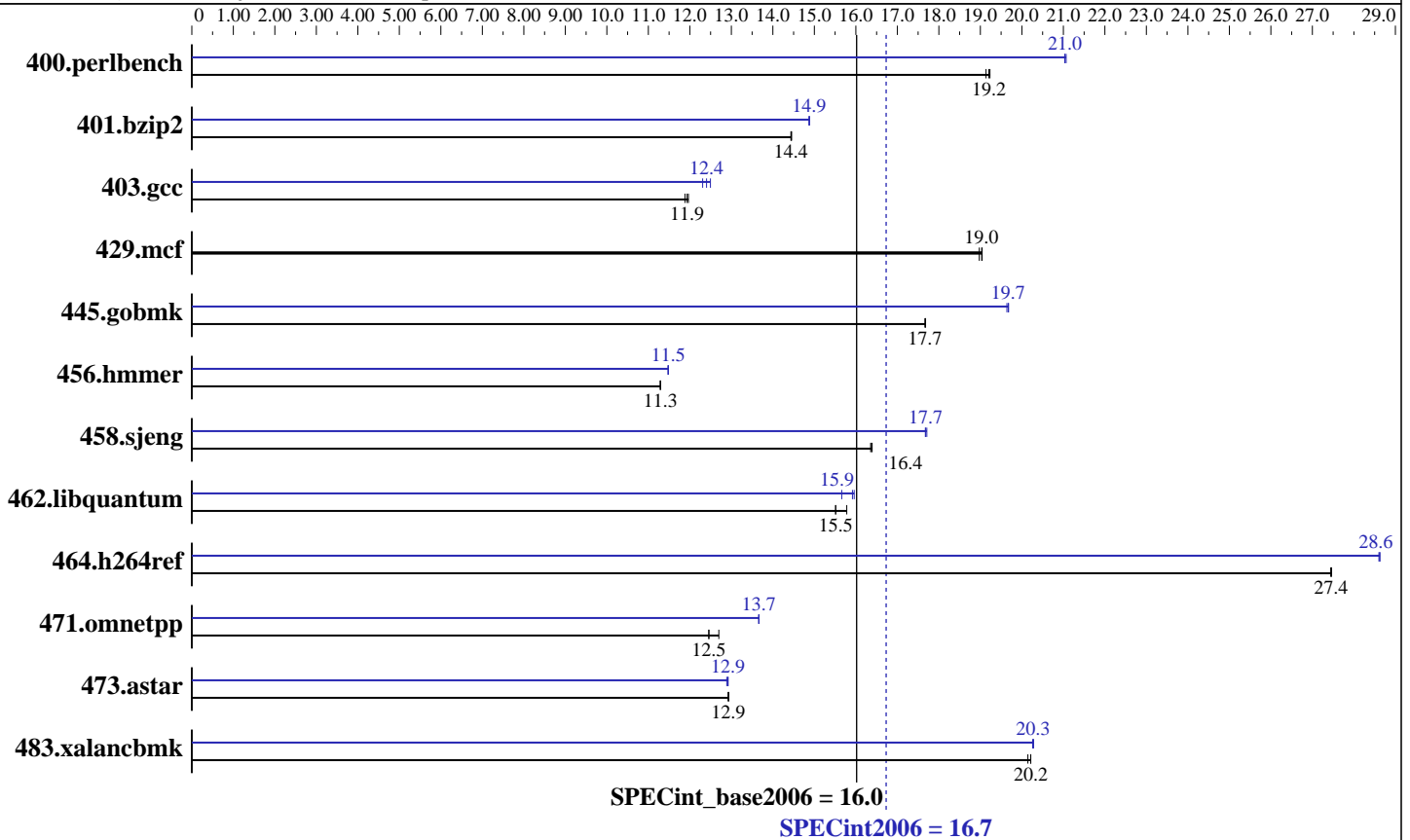
Test date: Feb-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Jan-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: X5355
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5,with ECC)
 Disk Subsystem: SATA II 7200 rpm
 Other Hardware: None

Software

Operating System: Windows XP, 64 bit Edition
 Compiler: Intel C++ Compiler for 32-bit applications, - version 9.1, Build 20070109Z
 Microsoft Visual Studio .NET 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	511	19.1	<u>509</u>	<u>19.2</u>	508	19.2	464	21.1	465	21.0	<u>464</u>	<u>21.0</u>
401.bzip2	<u>668</u>	<u>14.4</u>	668	14.5	669	14.4	649	14.9	<u>649</u>	<u>14.9</u>	648	14.9
403.gcc	<u>675</u>	<u>11.9</u>	678	11.9	673	12.0	644	12.5	<u>649</u>	<u>12.4</u>	654	12.3
429.mcf	481	19.0	479	19.0	<u>479</u>	<u>19.0</u>	481	19.0	479	19.0	<u>479</u>	<u>19.0</u>
445.gobmk	594	17.7	593	17.7	<u>594</u>	<u>17.7</u>	533	19.7	<u>533</u>	<u>19.7</u>	534	19.6
456.hammer	826	11.3	<u>826</u>	<u>11.3</u>	826	11.3	813	11.5	<u>813</u>	<u>11.5</u>	813	11.5
458.sjeng	740	16.4	<u>739</u>	<u>16.4</u>	738	16.4	<u>684</u>	<u>17.7</u>	683	17.7	685	17.7
462.libquantum	1336	15.5	<u>1336</u>	<u>15.5</u>	1313	15.8	<u>1302</u>	<u>15.9</u>	1323	15.7	1298	16.0
464.h264ref	<u>806</u>	<u>27.4</u>	806	27.4	806	27.5	773	28.6	774	28.6	<u>773</u>	<u>28.6</u>
471.omnetpp	502	12.5	492	12.7	<u>502</u>	<u>12.5</u>	458	13.7	<u>457</u>	<u>13.7</u>	457	13.7
473.astar	<u>543</u>	<u>12.9</u>	543	12.9	543	12.9	<u>543</u>	<u>12.9</u>	545	12.9	543	12.9
483.xalancbmk	343	20.1	341	20.2	<u>341</u>	<u>20.2</u>	340	20.3	341	20.3	<u>340</u>	<u>20.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used, except:
Snoop Filter Enabled
Adjacent Cache Line Prefetch Enabled

General Notes

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:

-fast -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx-features -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.xml



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For other inquiries, please contact webmaster@spec.org.

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