SPEC® CINT2006 Result

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IBM Corporation

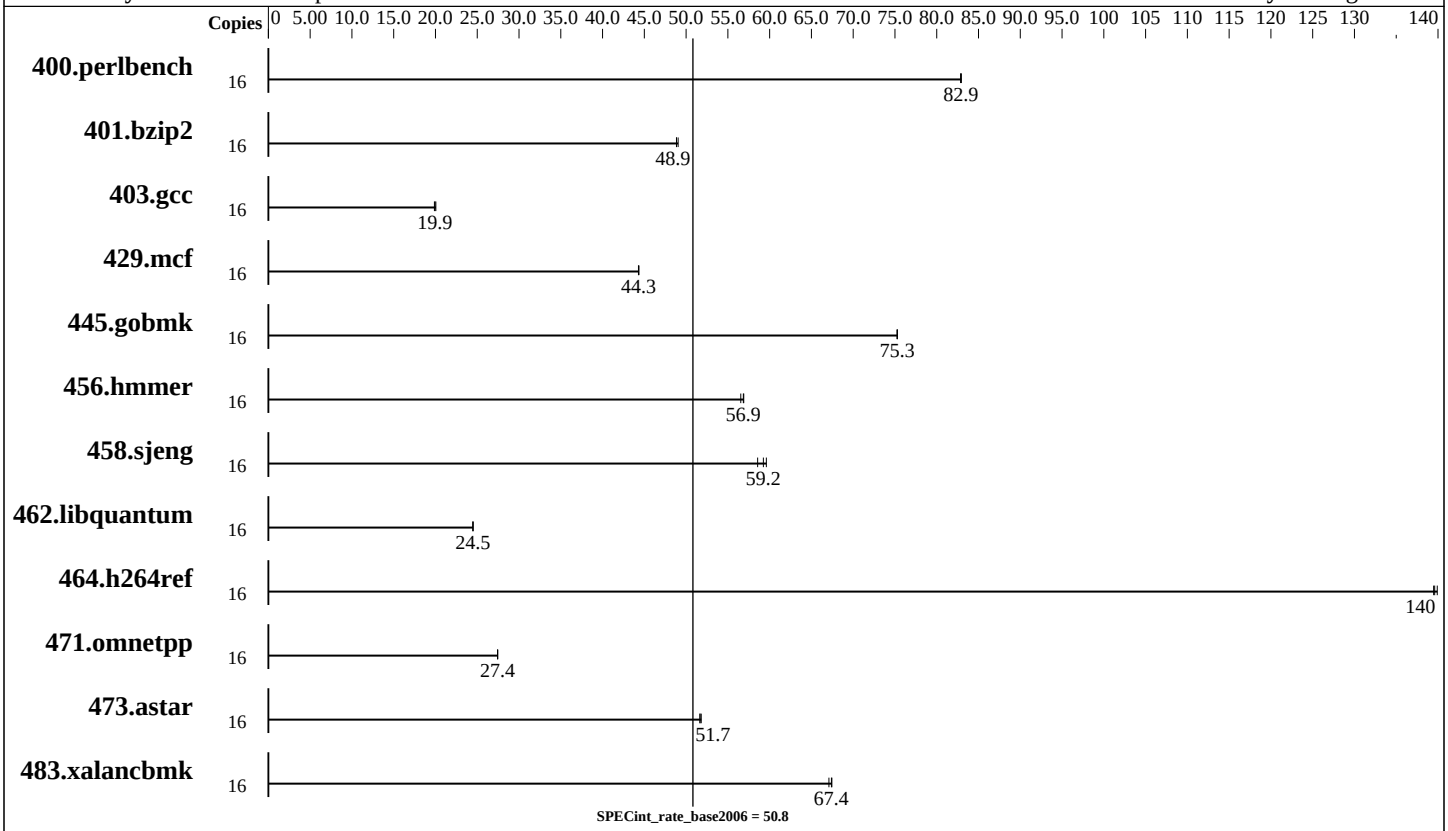
IBM System x3850 (Intel Xeon 7120N)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 50.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Feb-2007
Hardware Availability: Oct-2006
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 7120N
CPU Characteristics:	667 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1886	82.9	1885	82.9	**1886**	**82.9**							
401.bzip2	16	3148	49.0	**3160**	**48.9**	3161	48.8							
403.gcc	16	6426	20.0	6481	19.9	**6458**	**19.9**							
429.mcf	16	3294	44.3	**3292**	**44.3**	3291	44.3							
445.gobmk	16	**2230**	**75.3**	2230	75.3	2230	75.3							
456.hmmer	16	2624	56.9	2640	56.5	**2626**	**56.9**							
458.sjeng	16	3306	58.6	**3268**	**59.2**	3248	59.6							
462.libquantum	16	13538	24.5	13536	24.5	**13538**	**24.5**							
464.h264ref	16	2531	140	2539	139	**2536**	**140**							
471.omnetpp	16	3645	27.4	3643	27.4	**3645**	**27.4**							
473.astar	16	**2174**	**51.7**	2168	51.8	2175	51.6							
483.xalancbmk	16	1637	67.4	**1638**	**67.4**	1645	67.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml`

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