SPEC® CFP2006 Result

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Hewlett-Packard Company

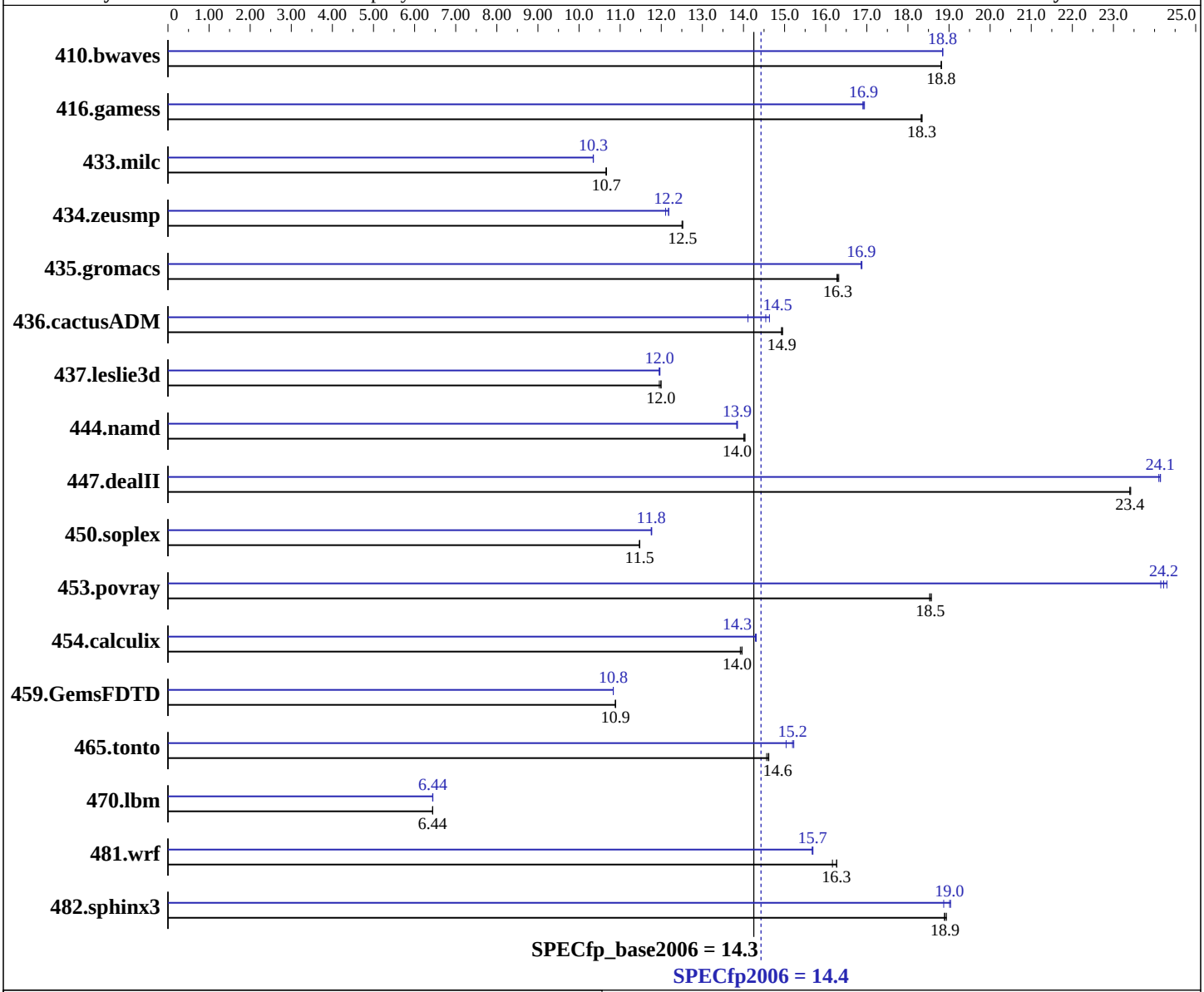
ProLiant ML370 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®2006 = 14.4

SPECfp_base2006 = 14.3

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	722	18.8	722	18.8	**722**	**18.8**	721	18.9	721	18.8	**721**	**18.8**
416.gamess	**1068**	**18.3**	1069	18.3	1067	18.3	1158	16.9	**1157**	**16.9**	1156	16.9
433.milc	**861**	**10.7**	861	10.7	861	10.7	888	10.3	**887**	**10.3**	887	10.4
434.zeusmp	**727**	**12.5**	727	12.5	728	12.5	747	12.2	752	12.1	**747**	**12.2**
435.gromacs	438	16.3	439	16.3	**438**	**16.3**	**423**	**16.9**	423	16.9	423	16.9
436.cactusADM	**800**	**14.9**	801	14.9	799	15.0	**822**	**14.5**	847	14.1	817	14.6
437.leslie3d	**784**	**12.0**	784	12.0	786	12.0	**786**	**12.0**	785	12.0	787	11.9
444.namd	572	14.0	**572**	**14.0**	571	14.0	**579**	**13.9**	580	13.8	579	13.9
447.dealII	**489**	**23.4**	489	23.4	488	23.4	475	24.1	**474**	**24.1**	474	24.1
450.soplex	727	11.5	727	11.5	**727**	**11.5**	**709**	**11.8**	709	11.8	709	11.8
453.povray	286	18.6	287	18.5	**287**	**18.5**	**220**	**24.2**	219	24.3	220	24.2
454.calculix	591	14.0	**591**	**14.0**	593	13.9	577	14.3	**577**	**14.3**	577	14.3
459.GemsFDTD	**975**	**10.9**	975	10.9	975	10.9	979	10.8	**979**	**10.8**	980	10.8
465.tonto	673	14.6	**674**	**14.6**	676	14.6	654	15.0	646	15.2	**648**	**15.2**
470.lbm	2133	6.44	2134	6.44	**2134**	**6.44**	2132	6.45	**2133**	**6.44**	2134	6.44
481.wrf	691	16.2	687	16.3	**687**	**16.3**	713	15.7	712	15.7	**712**	**15.7**
482.sphinx3	1029	18.9	1032	18.9	**1031**	**18.9**	1024	19.0	**1025**	**19.0**	1033	18.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"start /b /wait /affinity" used to bind processes to CPU(s).
"ulimit -s unlimited" set
Single processor kernel used.
```

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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