Hewlett-Packard Company

ProLiant BL20p G4
(1.86 GHz, Intel Xeon processor E5320)

SPEClnt_rate2006 = 61.1
SPEClnt_rate_base2006 = 59.2

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

400.perlbench
401.bzip2
403.gcc
429.mcf
445.gobmk
456.hmmer
458.sjeng
462.libquantum
464.h264ref
471.omnetpp
473.astar
483.xalancbmk

SPEClnt_rate_base2006 = 59.2

SPECint_rate2006 = 61.1

Hardware

CPU Name: Intel Xeon E5320
CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB 1+D on chip per core, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem: 2x72 GB 10k SAS
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise x64 Edition SP1
Compiler: Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z
Package ID: W_CC_C_9.1.033
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0
SPEC CINT2006 Result

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SPECint_rate2006 = 61.1
SPECint_rate_base2006 = 59.2

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>8</td>
<td>750</td>
<td>104</td>
<td>750</td>
<td>104</td>
<td>750</td>
<td>104</td>
<td>8</td>
<td>688</td>
<td>114</td>
<td>687</td>
<td>114</td>
<td>687</td>
<td>114</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>8</td>
<td>1320</td>
<td>58.5</td>
<td>1317</td>
<td>58.6</td>
<td>1319</td>
<td>58.5</td>
<td>8</td>
<td>1296</td>
<td>59.6</td>
<td>1295</td>
<td>59.6</td>
<td>1297</td>
<td>59.5</td>
</tr>
<tr>
<td>403.gcc</td>
<td>8</td>
<td>2516</td>
<td>25.6</td>
<td>2486</td>
<td>25.9</td>
<td>2411</td>
<td>26.7</td>
<td>8</td>
<td>2522</td>
<td>25.5</td>
<td>2487</td>
<td>25.9</td>
<td>2401</td>
<td>26.8</td>
</tr>
<tr>
<td>429.mcf</td>
<td>8</td>
<td>1464</td>
<td>49.8</td>
<td>1466</td>
<td>49.8</td>
<td>1471</td>
<td>49.6</td>
<td>8</td>
<td>1464</td>
<td>49.8</td>
<td>1466</td>
<td>49.8</td>
<td>1471</td>
<td>49.6</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>8</td>
<td>888</td>
<td>94.6</td>
<td>887</td>
<td>94.6</td>
<td>887</td>
<td>94.6</td>
<td>8</td>
<td>791</td>
<td>106</td>
<td>791</td>
<td>106</td>
<td>789</td>
<td>106</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>8</td>
<td>1200</td>
<td>62.2</td>
<td>1200</td>
<td>62.2</td>
<td>1200</td>
<td>62.2</td>
<td>8</td>
<td>1168</td>
<td>63.9</td>
<td>1168</td>
<td>63.9</td>
<td>1168</td>
<td>63.9</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>8</td>
<td>1069</td>
<td>90.5</td>
<td>1069</td>
<td>90.6</td>
<td>1069</td>
<td>90.5</td>
<td>8</td>
<td>983</td>
<td>98.5</td>
<td>983</td>
<td>98.5</td>
<td>983</td>
<td>98.4</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>8</td>
<td>7089</td>
<td>23.4</td>
<td>7089</td>
<td>23.4</td>
<td>7089</td>
<td>23.4</td>
<td>8</td>
<td>7086</td>
<td>23.4</td>
<td>7085</td>
<td>23.4</td>
<td>7087</td>
<td>23.4</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>8</td>
<td>1164</td>
<td>152</td>
<td>1164</td>
<td>152</td>
<td>1165</td>
<td>152</td>
<td>8</td>
<td>1141</td>
<td>155</td>
<td>1141</td>
<td>155</td>
<td>1141</td>
<td>155</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>8</td>
<td>1409</td>
<td>35.5</td>
<td>1410</td>
<td>35.5</td>
<td>1410</td>
<td>35.5</td>
<td>8</td>
<td>1395</td>
<td>35.8</td>
<td>1397</td>
<td>35.8</td>
<td>1398</td>
<td>35.8</td>
</tr>
<tr>
<td>473.astar</td>
<td>8</td>
<td>1166</td>
<td>48.1</td>
<td>1170</td>
<td>48.0</td>
<td>1166</td>
<td>48.2</td>
<td>8</td>
<td>1173</td>
<td>47.9</td>
<td>1175</td>
<td>47.8</td>
<td>1174</td>
<td>47.8</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>8</td>
<td>760</td>
<td>72.6</td>
<td>760</td>
<td>72.6</td>
<td>759</td>
<td>72.7</td>
<td>8</td>
<td>743</td>
<td>74.3</td>
<td>745</td>
<td>74.1</td>
<td>743</td>
<td>74.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F120000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
- -fast -Qcxx_features /F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Daalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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