SPEC® CINT2006 Result

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Hewlett-Packard Company

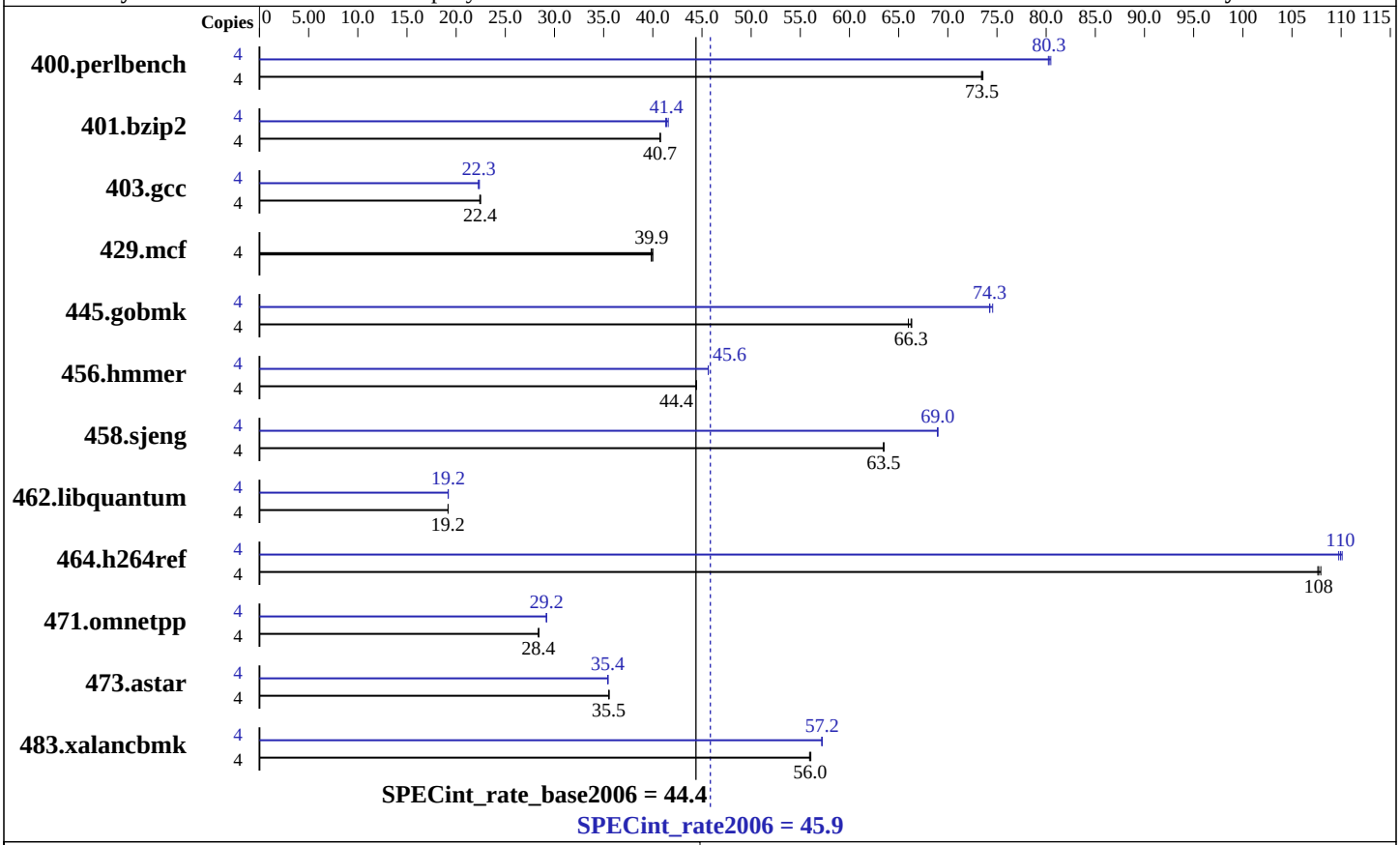
ProLiant DL380 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint®_rate2006 = 45.9

SPECint_rate_base2006 = 44.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem:	4x36 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise x64 Edition SP1
Compiler:	Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z Package ID: W_CC_C_9.1.033 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**532**	**73.5**	532	73.4	531	73.6	4	**487**	**80.3**	487	80.2	486	80.5
401.bzip2	4	**947**	**40.7**	947	40.8	947	40.7	4	929	41.6	934	41.3	**932**	**41.4**
403.gcc	4	1437	22.4	1432	22.5	**1435**	**22.4**	4	**1444**	**22.3**	1446	22.3	1440	22.4
429.mcf	4	**915**	**39.9**	916	39.8	912	40.0	4	**915**	**39.9**	916	39.8	912	40.0
445.gobmk	4	633	66.3	636	66.0	**633**	**66.3**	4	563	74.6	**565**	**74.3**	565	74.3
456.hmmer	4	**840**	**44.4**	840	44.4	840	44.4	4	**818**	**45.6**	818	45.6	818	45.6
458.sjeng	4	763	63.5	**763**	**63.5**	762	63.5	4	702	68.9	702	69.0	**702**	**69.0**
462.libquantum	4	**4321**	**19.2**	4322	19.2	4320	19.2	4	4318	19.2	**4318**	**19.2**	4320	19.2
464.h264ref	4	**822**	**108**	822	108	820	108	4	804	110	**805**	**110**	807	110
471.omnetpp	4	**881**	**28.4**	881	28.4	880	28.4	4	**856**	**29.2**	857	29.2	856	29.2
473.astar	4	790	35.5	790	35.5	**790**	**35.5**	4	793	35.4	**792**	**35.4**	792	35.4
483.xalancbmk	4	493	55.9	492	56.1	**493**	**56.0**	4	**482**	**57.2**	482	57.2	483	57.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For questions about this result, please contact the tester.
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