Itautec

Servidor Itautec MX221 (Intel Xeon E5320)

**SPECfp_rate2006 = 40.5**

**SPECfp_rate_base2006 = 40.5**

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec
**Test date:** Feb-2007
**Hardware Availability:** Feb-2007
**Software Availability:** May-2006

<table>
<thead>
<tr>
<th>SPEC Test</th>
<th>Results</th>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>416.gamess</td>
<td>8</td>
<td>CPU Characteristics: 1066MHz system bus</td>
<td>Compiler: Intel C++ Compiler for IA32 version 9.1</td>
</tr>
<tr>
<td>433.milc</td>
<td>8</td>
<td>CPU MHz: 1860</td>
<td>Package ID W_CC_C_9.1.025 Build no 20060519Z</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>8</td>
<td>FPU: Integrated</td>
<td>Intel Fortran Compiler for IA32 version 9.1</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>8</td>
<td>CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip</td>
<td>Package ID W_FC_C_9.1.025 Build no 20060519Z</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>8</td>
<td>CPU(s) orderable: 1-2 chips</td>
<td>Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>8</td>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Auto Parallel: No</td>
</tr>
<tr>
<td>444.namd</td>
<td>8</td>
<td>Secondary Cache: 8 MB I+D on chip per core, 4 MB shared / 2 cores</td>
<td>File System: NTFS</td>
</tr>
<tr>
<td>447.dealII</td>
<td>8</td>
<td>23.0</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>450.soplex</td>
<td>8</td>
<td>43.5</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>453.povray</td>
<td>8</td>
<td>64.6</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>454.calculix</td>
<td>8</td>
<td>17.5</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>8</td>
<td>36.0</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>465.tonto</td>
<td>8</td>
<td>37.1</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>470.lbm</td>
<td>8</td>
<td>42.8</td>
<td>Continued on next page</td>
</tr>
<tr>
<td>481.wrf</td>
<td>8</td>
<td></td>
<td>Continued on next page</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>8</td>
<td></td>
<td>Continued on next page</td>
</tr>
</tbody>
</table>

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SPEC CFP2006 Result

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SPECfp_rate_base2006 = 40.5

CPU2006 license: 9001
Test sponsor: Itautec
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Hardware Availability: Feb-2007
Tested by: Itautec
Software Availability: May-2006

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 73 GB SCSI, 10000RPM
Other Hardware: None

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>8</td>
<td>4310</td>
<td>25.2</td>
<td>4306</td>
<td>25.2</td>
<td>4306</td>
<td>25.2</td>
<td>4306</td>
<td>25.2</td>
<td>4306</td>
<td>25.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>416.gamess</td>
<td>8</td>
<td>1766</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>433.mile</td>
<td>8</td>
<td>4665</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>8</td>
<td>1517</td>
<td>48.0</td>
<td>1512</td>
<td>48.1</td>
<td>1503</td>
<td>48.4</td>
<td>1512</td>
<td>48.1</td>
<td>1503</td>
<td>48.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td>8</td>
<td>686</td>
<td>83.3</td>
<td>687</td>
<td>83.2</td>
<td>686</td>
<td>83.3</td>
<td>686</td>
<td>83.3</td>
<td>686</td>
<td>83.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>8</td>
<td>2403</td>
<td>39.8</td>
<td>2391</td>
<td>40.0</td>
<td>2430</td>
<td>39.3</td>
<td>2403</td>
<td>39.8</td>
<td>2391</td>
<td>40.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>8</td>
<td>4038</td>
<td>18.6</td>
<td>4040</td>
<td>18.6</td>
<td>4042</td>
<td>18.6</td>
<td>4038</td>
<td>18.6</td>
<td>4040</td>
<td>18.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>444.namd</td>
<td>8</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>447.dealII</td>
<td>8</td>
<td>1058</td>
<td>86.5</td>
<td>1074</td>
<td>85.2</td>
<td>1102</td>
<td>83.1</td>
<td>1074</td>
<td>85.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>450.soplex</td>
<td>8</td>
<td>2913</td>
<td>22.9</td>
<td>2905</td>
<td>23.0</td>
<td>2896</td>
<td>23.0</td>
<td>2913</td>
<td>22.9</td>
<td>2905</td>
<td>23.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>453.povray</td>
<td>8</td>
<td>403</td>
<td>106</td>
<td>403</td>
<td>106</td>
<td>402</td>
<td>106</td>
<td>403</td>
<td>106</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>454.calculix</td>
<td>8</td>
<td>1021</td>
<td>64.6</td>
<td>1021</td>
<td>64.6</td>
<td>1002</td>
<td>65.8</td>
<td>1021</td>
<td>64.6</td>
<td>1002</td>
<td>65.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>8</td>
<td>4865</td>
<td>17.4</td>
<td>4861</td>
<td>17.5</td>
<td>4858</td>
<td>17.5</td>
<td>4861</td>
<td>17.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>465.tonto</td>
<td>8</td>
<td>1810</td>
<td>43.5</td>
<td>1807</td>
<td>43.6</td>
<td>1824</td>
<td>43.2</td>
<td>1810</td>
<td>43.5</td>
<td>1807</td>
<td>43.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>470.lbm</td>
<td>8</td>
<td>5410</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>481.wrf</td>
<td>8</td>
<td>2488</td>
<td>35.9</td>
<td>2479</td>
<td>36.0</td>
<td>2464</td>
<td>36.3</td>
<td>2488</td>
<td>35.9</td>
<td>2479</td>
<td>36.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>8</td>
<td>4203</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

### Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Base Compiler Invocation (Continued)

Fortran benchmarks:
   ifort

Benchmarks using both Fortran and C:
   icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

Fortran benchmarks:
   -Qlowercase /assume:underscore
   -T

C benchmarks:
   -fast /F9500000000 shlSMPMt.lib
   -Qlowercase /assume:underscore

C++ benchmarks:
   -fast -Qcxx_features /F9500000000 shlSMPMt.lib
   -Qlowercase /assume:underscore

Fortran benchmarks:
   -fast /F9500000000
   -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
   -fast /F9500000000
   -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:
   433.milc: basepeak = yes
   470.lbm: basepeak = yes
   482.sphinx3: basepeak = yes

C++ benchmarks:
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Peak Optimization Flags (Continued)

444.namd: basepeak = yes
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: basepeak = yes

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: basepeak = yes

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

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Tested with SPEC CPU2006 v1.0.