SPEC® CFP2006 Result

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Hewlett-Packard Company

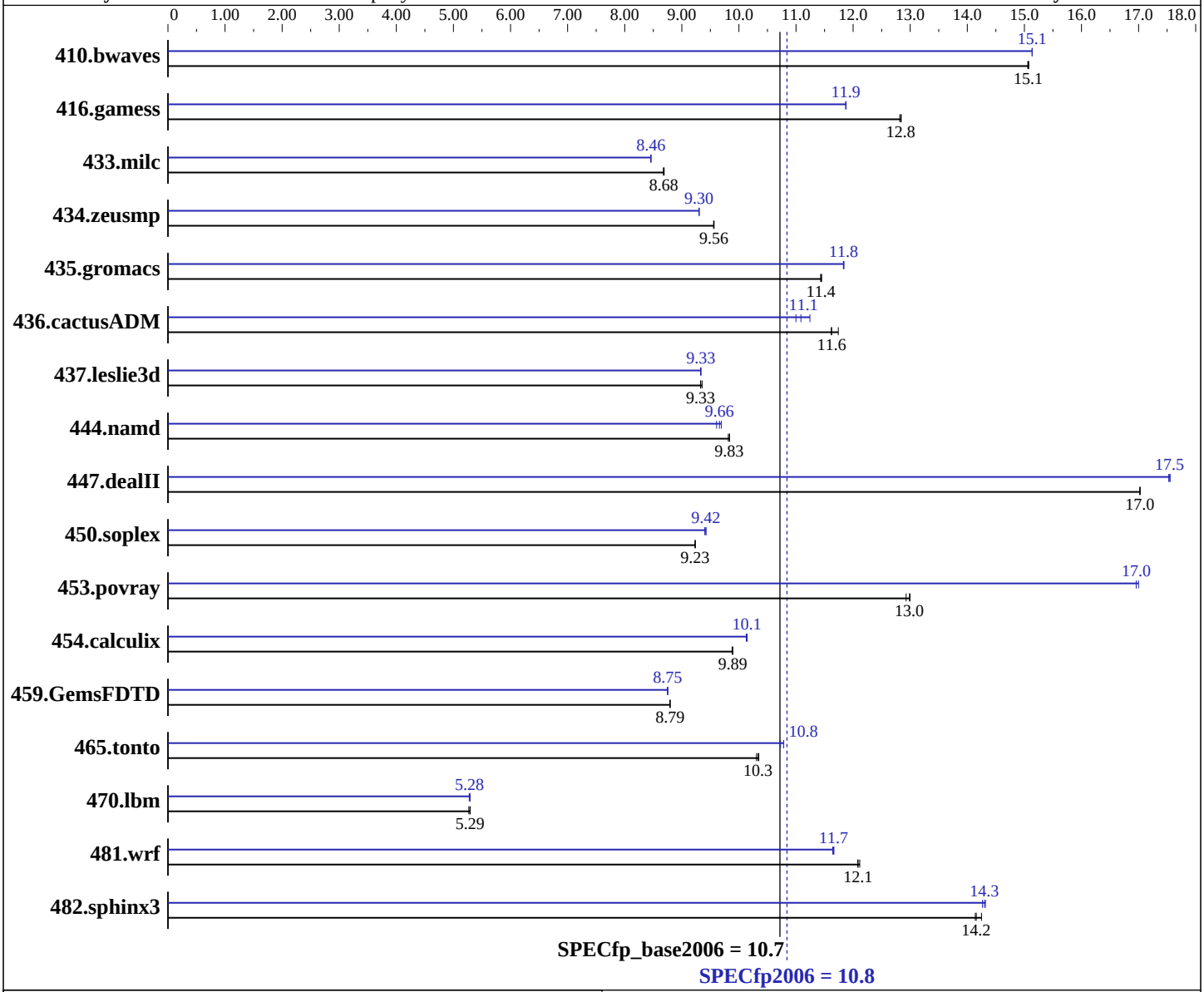
ProLiant ML350 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp®2006 = 10.8

SPECfp_base2006 = 10.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	8 GB (4x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	4x36 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	901	15.1	**902**	**15.1**	902	15.1	898	15.1	898	15.1	**898**	**15.1**
416.gamess	1528	12.8	1525	12.8	**1525**	**12.8**	1650	11.9	**1649**	**11.9**	1648	11.9
433.milc	1058	8.68	1056	8.69	**1057**	**8.68**	1086	8.46	1085	8.46	**1085**	**8.46**
434.zeusmp	952	9.56	**952**	**9.56**	951	9.57	**978**	**9.30**	978	9.30	978	9.30
435.gromacs	625	11.4	624	11.4	**624**	**11.4**	603	11.8	604	11.8	**603**	**11.8**
436.cactusADM	1018	11.7	**1028**	**11.6**	1029	11.6	1086	11.0	**1078**	**11.1**	1063	11.2
437.leslie3d	**1007**	**9.33**	1008	9.33	1005	9.36	**1007**	**9.33**	1007	9.34	1008	9.33
444.namd	817	9.81	**816**	**9.83**	815	9.84	828	9.69	835	9.61	**830**	**9.66**
447.dealII	672	17.0	672	17.0	**672**	**17.0**	652	17.6	653	17.5	**652**	**17.5**
450.soplex	903	9.23	903	9.23	**903**	**9.23**	**885**	**9.42**	887	9.40	885	9.42
453.povray	411	12.9	409	13.0	**410**	**13.0**	313	17.0	**314**	**17.0**	314	17.0
454.calculix	835	9.88	**834**	**9.89**	834	9.89	813	10.1	814	10.1	**813**	**10.1**
459.GemsFDTD	1206	8.80	1207	8.79	**1207**	**8.79**	1212	8.76	**1212**	**8.75**	1213	8.75
465.tonto	951	10.3	**952**	**10.3**	955	10.3	**912**	**10.8**	912	10.8	917	10.7
470.lbm	2596	5.29	2608	5.27	**2598**	**5.29**	2596	5.29	**2601**	**5.28**	2604	5.28
481.wrf	**924**	**12.1**	924	12.1	922	12.1	959	11.6	**958**	**11.7**	958	11.7
482.sphinx3	1368	14.2	**1377**	**14.2**	1378	14.1	1361	14.3	**1363**	**14.3**	1366	14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
"ulimit -s unlimited" set
Single processor kernel used
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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